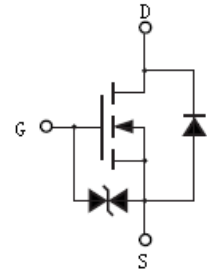


Features

- Low power loss by high speed switching and low on-resistance
- Excellent thermal behavior
- Very low FOM for fast switching efficiency
- Product validation acc. JEDEC Standard
- Integrated ESD protection diode: HBM: JESD22-A114-B: 3A
- RoHS compliant with Halogen-free

HF

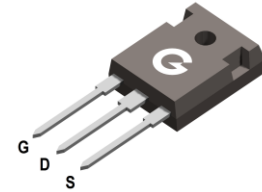


Applications

- LED lighting
- Switching applications
- Industrial power
- PFC stage

Mechanical Data

- Case: TO-247
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208



TO-247

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
SJM90R300U	TO-247	30 pcs / Tube	SJM90R300U

Maximum Ratings

(@ $T_C = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DSS}	900	V
Gate-to-Source Voltage (Static)	V_{GSS}	± 20	V
Gate-to-Source Voltage (Dynamic, AC ($f > 1\text{Hz}$))		± 30	V
Continuous Drain Current ($T_C = 25^\circ\text{C}$)	I_D	18	A
Continuous Drain Current ($T_C = 100^\circ\text{C}$)		11	A
Pulsed Drain Current ($t_p = 10\mu\text{s}$, $T_C = 25^\circ\text{C}$)	I_{DM}	72	A
Single Pulse Avalanche Energy ²	E_{AS}	500	mJ
Power Dissipation ($T_C = 25^\circ\text{C}$)	P_D	250	W
Operating Junction Temperature Range	T_J	-55 ~ +150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55 ~ +150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance Junction-to-Case	$R_{\theta JC}$	-	0.3	0.5	$^{\circ}\text{C}/\text{W}$
Thermal Resistance Junction-to-Air	$R_{\theta JA}$	-	-	40	$^{\circ}\text{C}/\text{W}$

Electrical Characteristics (@ $T_A = 25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
V_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	900	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 900\text{V}, V_{GS} = 0\text{V}$	-	-	1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$	-	-	± 1	μA
On Characteristics						
$R_{DS(ON)}$	Drain-Source On-resistance *1	$V_{GS} = 10\text{V}, I_D = 5\text{A}$	-	0.22	0.3	Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	2	2.9	4	V
R_G	Gate Resistance	$V_{GS} = 0\text{V}, f = 1\text{MHz}$	-	2.7	-	Ω
Dynamic Characteristics						
C_{ISS}	Input Capacitance	$V_{GS} = 0\text{V}$	-	1935	-	pF
C_{OSS}	Output Capacitance	$V_{DS} = 40\text{V}$	-	106	-	
C_{RSS}	Reverse Transfer Capacitance	$f = 250\text{kHz}$	-	3.8	-	
Switching Characteristics						
$t_{d(ON)}$	Turn-on Delay Time	$V_{DD} = 450\text{V}$	-	21	-	ns
t_r	Turn-on Rise Time	$V_{GS} = 15\text{V}$	-	28	-	
$t_{d(OFF)}$	Turn-Off Delay Time	$I_D = 5\text{A}$	-	80	-	
t_f	Turn-Off Fall Time	$R_G = 3.3\Omega$	-	75	-	
Q_G	Total Gate-Charge	$V_{DD} = 720\text{V}$	-	48	-	nC
Q_{GS}	Gate to Source Charge	$V_{GS} = 10\text{V}$	-	8.4	-	
Q_{GD}	Gate to Drain (Miller) Charge	$I_D = 9\text{A}$	-	18.4	-	
Source-Drain Diode Characteristics						
V_{SD}	Diode Forward Voltage *1	$I_{SD} = 9\text{A}, V_{GS} = 0\text{V}$	-	0.85	1.4	V
t_{rr}	Reverse Recovery Time	$I_F = 3\text{A}, V_R = 400\text{V}$	-	290	-	ns
Q_{rr}	Reverse Recovery Charge	$di/dt = 100\text{A}/\mu\text{s}$	-	2.9	-	μC

Notes:

- The data tested by pulsed, pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- The E_{AS} data shows Max. rating. The test condition is $V_{DD} = 100\text{V}, V_{GS} = 15\text{V}, L = 50\text{mH}$

Ratings and Characteristics Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

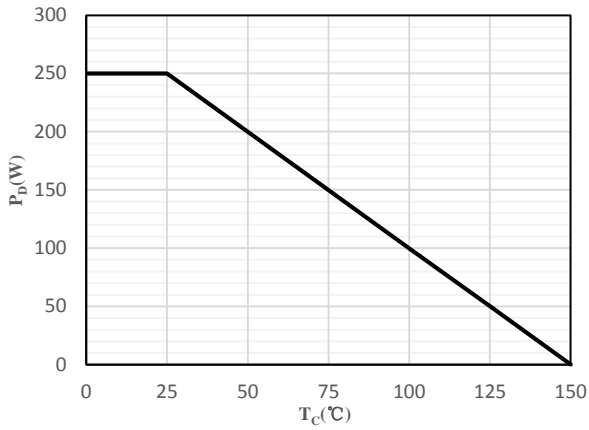


Fig 1 Power Dissipation

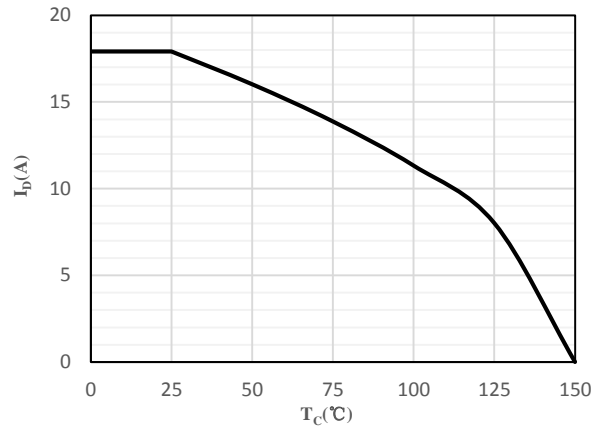


Fig 2 Drain Current

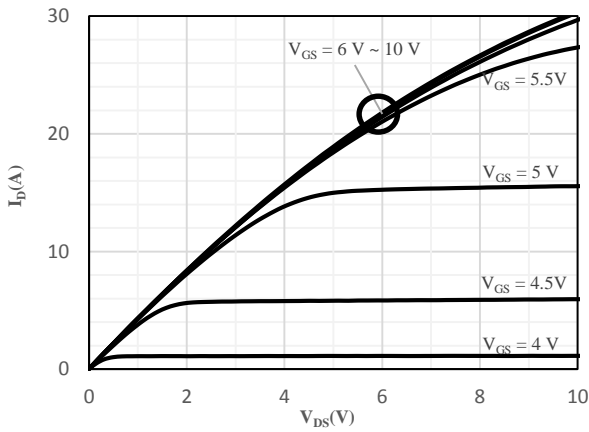


Fig 3 Typical Output Characteristics

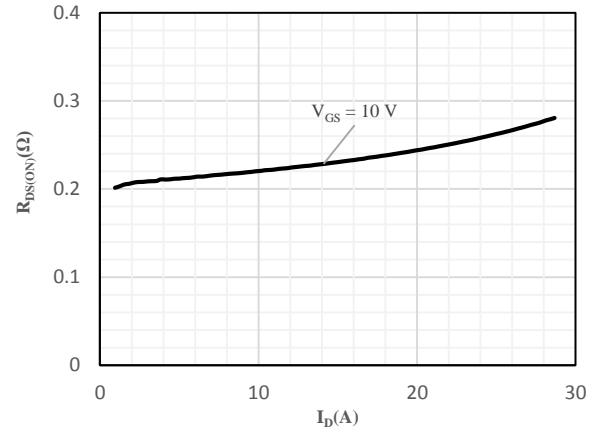


Fig 4 On-Resistance vs. Drain Current and Gate Voltage

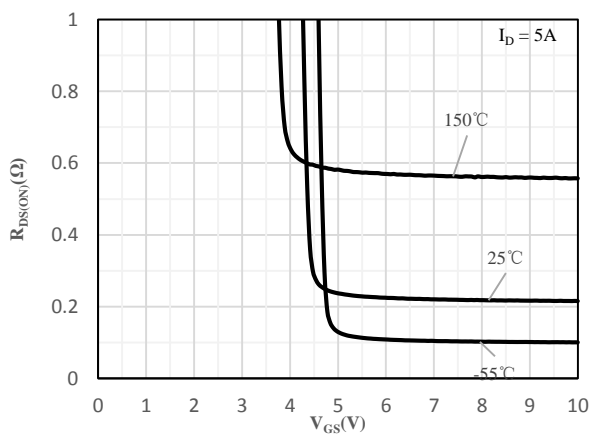


Fig 5 On-Resistance vs. Gate-Source Voltage

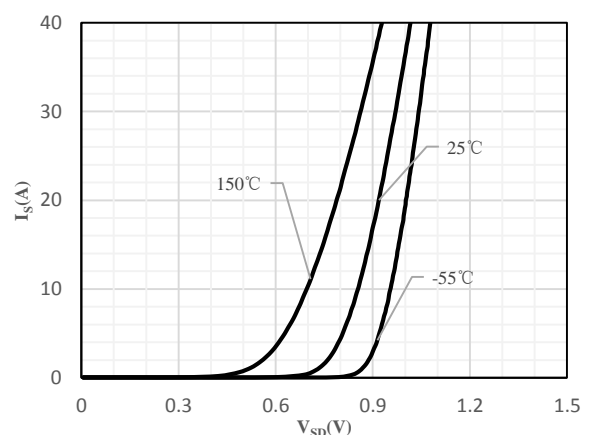


Fig 6 Body-Diode Characteristics

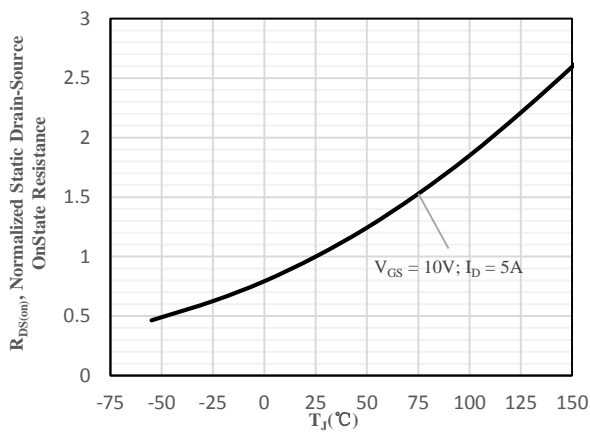


Fig 7 Normalized On-Resistance vs. Junction Temperature

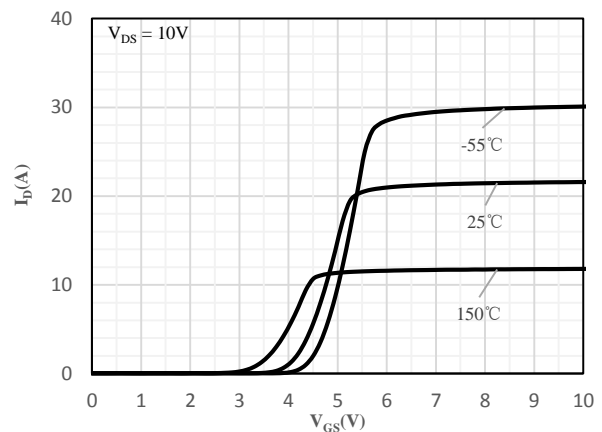


Fig 8 Transfer Characteristics

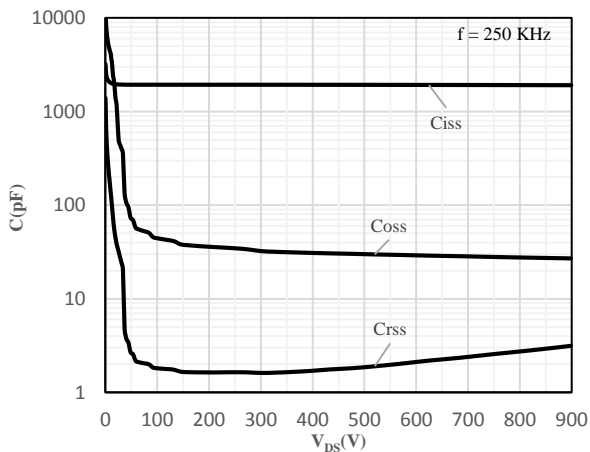


Fig 9 Capacitance Characteristics

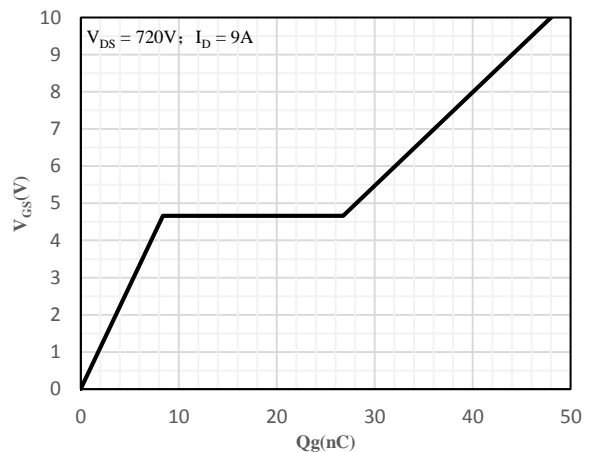


Fig 10 Gate-Charge Characteristics

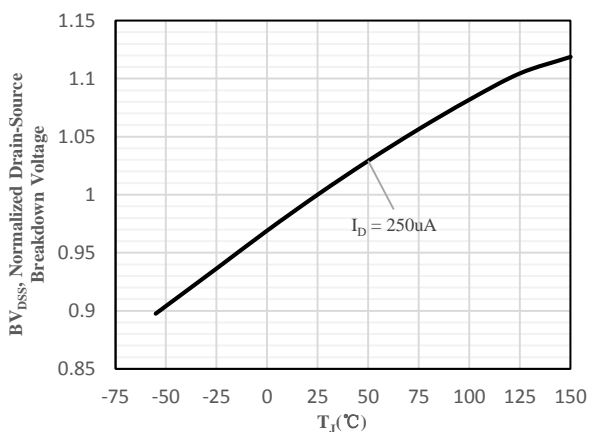


Fig 11 Normalized Breakdown Voltage vs. Junction Temperature

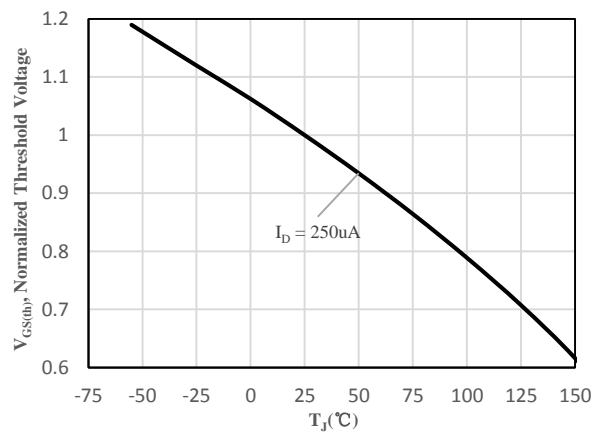


Fig 12 Normalized $V_{GS(th)}$ vs. Junction Temperature

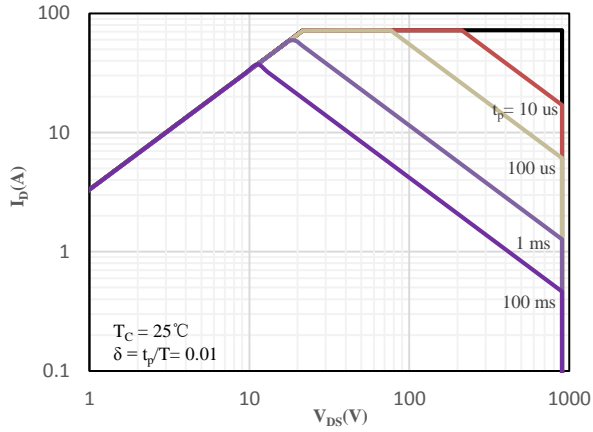


Fig 13 Safe Operating Area

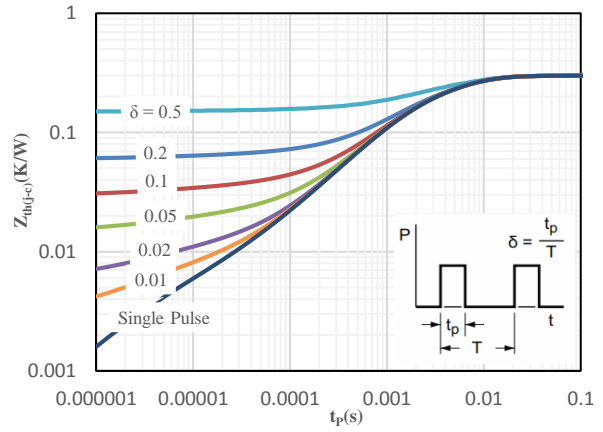
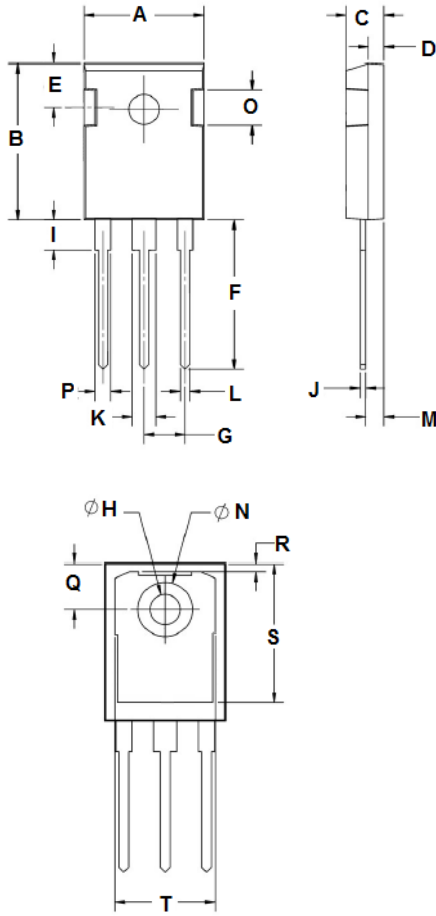


Fig 14 Maximum transient thermal impedance

Package Outline Dimensions (Unit: mm)



TO-247		
Dimension	Min.	Max.
A	15.50	16.10
B	20.70	21.30
C	4.70	5.30
D	1.80	2.20
E	5.20	5.80
F	19.70	20.30
G	5.20	5.60
H	3.30	3.70
I	3.90	4.30
J	0.50	0.70
K	2.80	3.20
L	1.00	1.40
M	2.20	2.60
N	7.00	7.20
O	4.90	5.30
P	1.80	2.20
Q	5.70	5.90
R	0.80	1.20
S	17.00	17.80
T	13.60	14.20