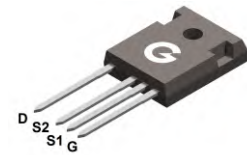
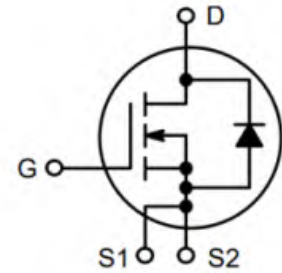


Features

- High blocking voltage with low on-resistance
- High-speed switching with low capacitances
- Low switching loss
- Sense pin for optimized switching performance
- RoHS compliant with Halogen-free

HF



TO-247-4L

Applications

- Solar inverters
- Charger
- Uninterruptible Power Supply
- Switch Mode Power Supplies

Mechanical Data

- Case: TO-247-4L
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
GSC65R060U4	TO-247-4L	30 pcs / Tube	GSC65R060U4

Maximum Ratings (@ T_C = 25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V _{DSS}	650	V
Gate-to-Source Voltage (transient)	V _{GSS}	-8/+22	V
Gate-to-Source Voltage (static)		-4/+18	V
Continuous Drain Current (T _C = 25°C)	I _D	51	A
Continuous Drain Current (T _C = 100°C)		36	A
Pulsed Drain Current	I _{DM}	100	A
Power Dissipation (T _C = 25°C)	P _D	178	W
Operating Junction Temperature Range	T _J	-40 ~ +175	°C
Storage Temperature Range	T _{STG}	-40 ~ +175	°C

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance Junction-to-Case	R _{θJC}	-	-	0.84	°C/W
Thermal Resistance Junction-to-Air	R _{θJA}	-	-	50	°C/W

Electrical Characteristics (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
V_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 100\mu A$	650	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 650V, V_{GS} = 0V$	-	-	10	μA
I_{GSS}	Gate-Body Leakage Current	$V_{GS} = 18V, V_{DS} = 0V$	-	-	250	nA
		$V_{GS} = -4V, V_{DS} = 0V$	-	-	-250	nA
On Characteristics						
$R_{DS(ON)}$	Drain-Source On-resistance *1	$V_{GS} = 18V, I_D = 20A, T_J = 25^\circ\text{C}$	-	60	-	m Ω
		$V_{GS} = 18V, I_D = 20A, T_J = 175^\circ\text{C}$	-	63	-	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 5mA$	2	-	4	V
R_G	Gate Resistance	$V_{GS} = 0V, f = 1MHz$	-	2.1	-	Ω
Dynamic Characteristics						
C_{ISS}	Input Capacitance	$V_{GS} = 0V$	-	1301	-	pF
C_{OSS}	Output Capacitance	$V_{DS} = 600V$	-	138	-	
C_{RSS}	Reverse Transfer Capacitance	$f = 1MHz$	-	14	-	
Switching Characteristics						
$t_{d(ON)}$	Turn-on Delay Time *2	$V_{DD} = 400V$ $V_{GS} = -4/18V$ $I_D = 20A$ $R_G = 2.5\Omega$	-	10	-	ns
t_r	Turn-on Rise Time *2		-	13	-	
$t_{d(OFF)}$	Turn-Off Delay Time *2		-	17	-	
t_f	Turn-Off Fall Time *2		-	7	-	μJ
E_{on}	Turn-On Energy *2		-	56	-	
E_{off}	Turn-Off Energy *2		-	7	-	
Q_G	Total Gate-Charge	$V_{DD} = 400V$	-	68	-	nC
Q_{GS}	Gate to Source Charge	$V_{GS} = -4/18V$	-	17	-	
Q_{GD}	Gate to Drain (Miller) Charge	$I_D = 20A$	-	22	-	
Source-Drain Diode Characteristics						
V_{SD}	Diode Forward Voltage *1	$I_{SD} = 20A, V_{GS} = -4V, T_J = 25^\circ\text{C}$	-	5.4	-	V
		$I_{SD} = 20A, V_{GS} = -4V, T_J = 175^\circ\text{C}$	-	4.6	-	
t_{rr}	Reverse Recovery Time	$I_F = 20A, V_R = 400V$	-	18	-	ns
Q_{rr}	Reverse Recovery Charge	$d_i/d_t = 989A/\mu s$	-	104	-	nC

Notes:

- The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- Guaranteed by design, not subject to production

Ratings and Characteristics Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

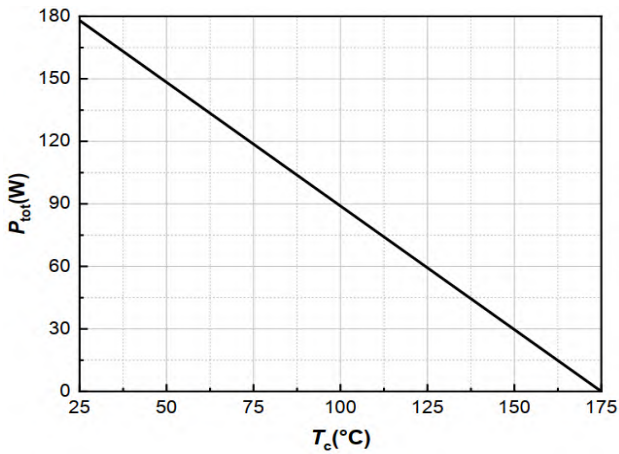


Fig 1 Power Dissipation

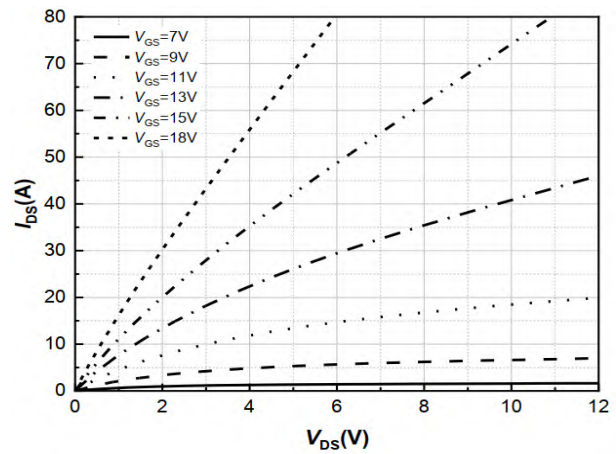


Fig 2 Typical Output Characteristics ($T_j = -40^\circ\text{C}$)

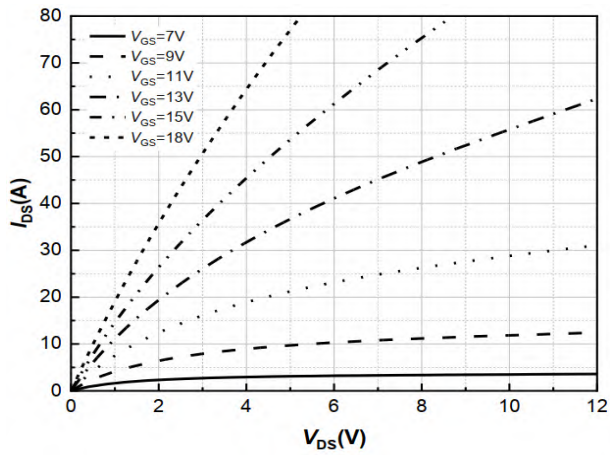


Fig 3 Typical Output Characteristics ($T_j = 25^\circ\text{C}$)

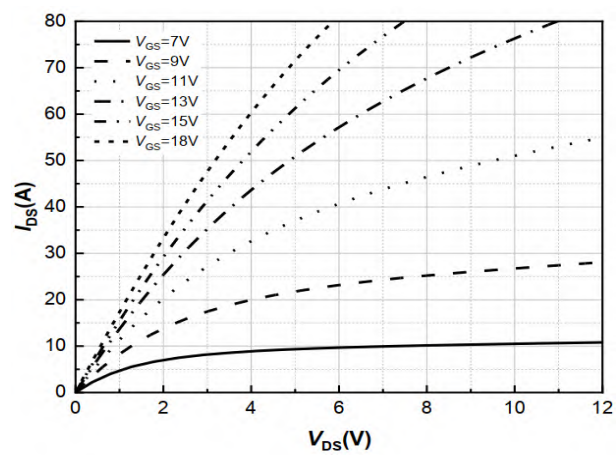


Fig 4 Typical Output Characteristics ($T_j = 175^\circ\text{C}$)

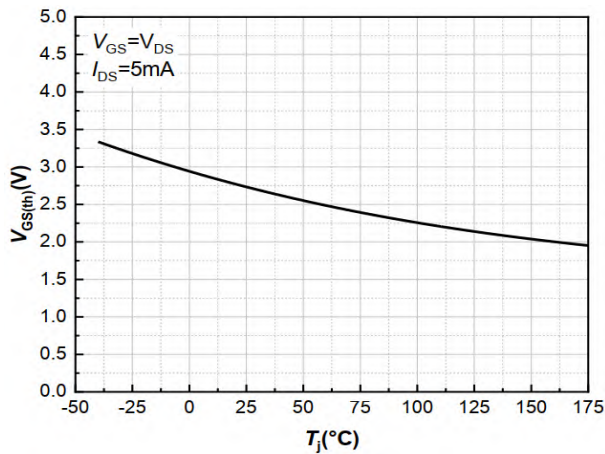


Fig 5 $V_{GS(th)}$ vs. Junction Temperature

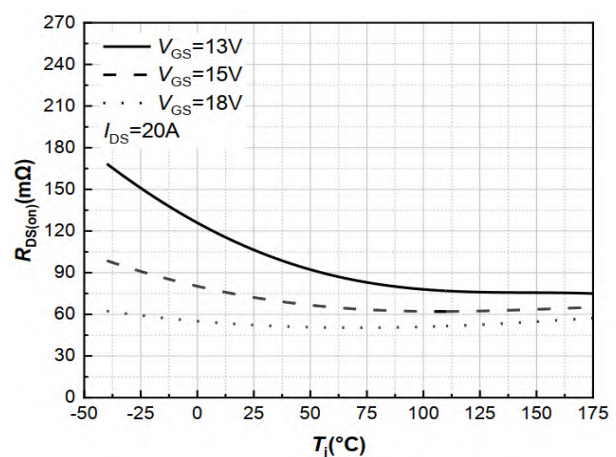


Fig 6 On-Resistance vs. Junction Temperature

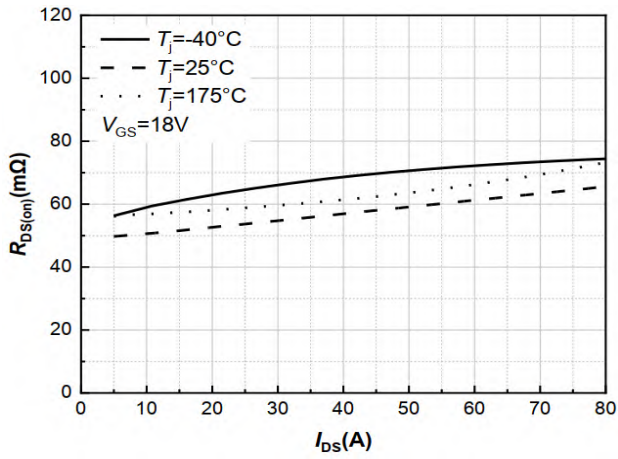


Fig 7 On-Resistance vs. Drain Current

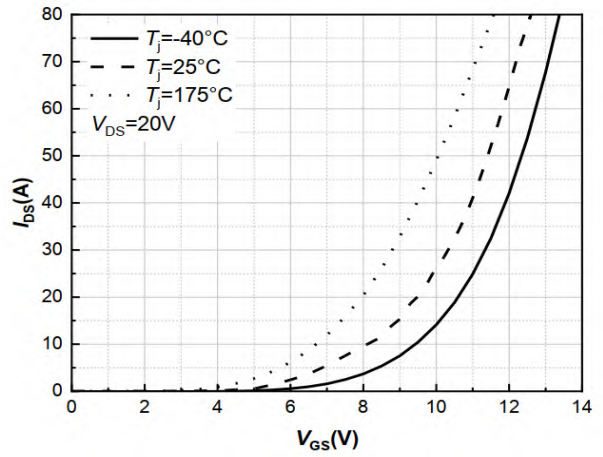


Fig 8 Transfer Characteristics

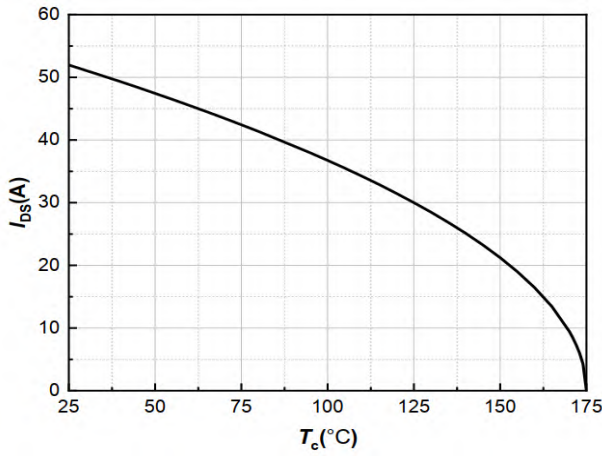


Fig 9 Drain Current

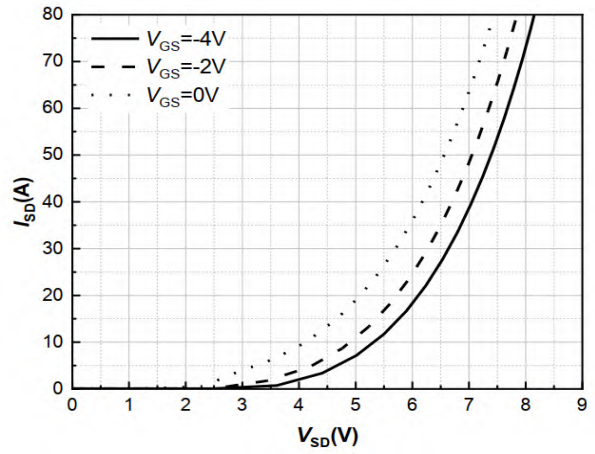


Fig 10 Body-Diode Characteristics ($T_J = -40^\circ\text{C}$)

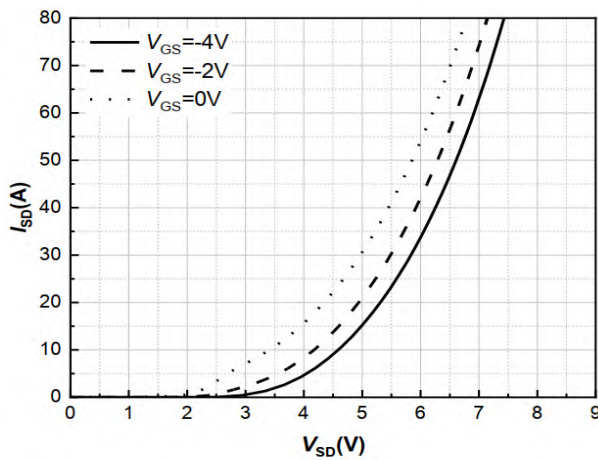


Fig 11 Body-Diode Characteristics ($T_J = 25^\circ\text{C}$)

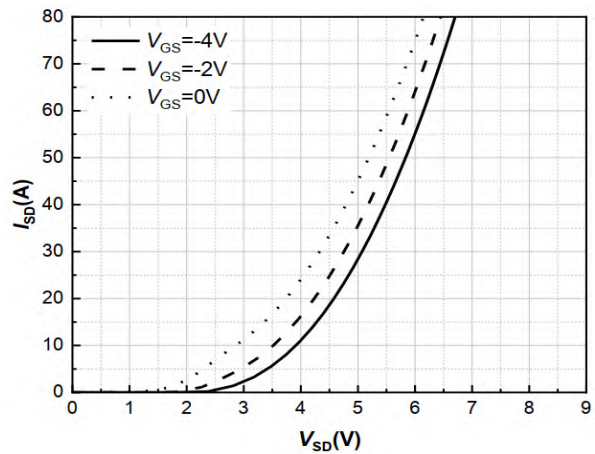


Fig 12 Body-Diode Characteristics ($T_J = 175^\circ\text{C}$)

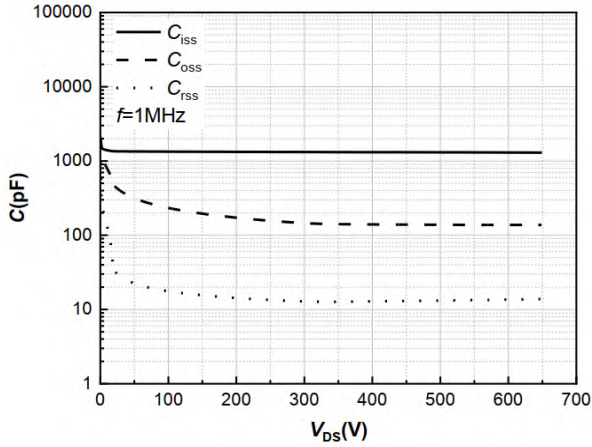


Fig 13 Capacitance Characteristics

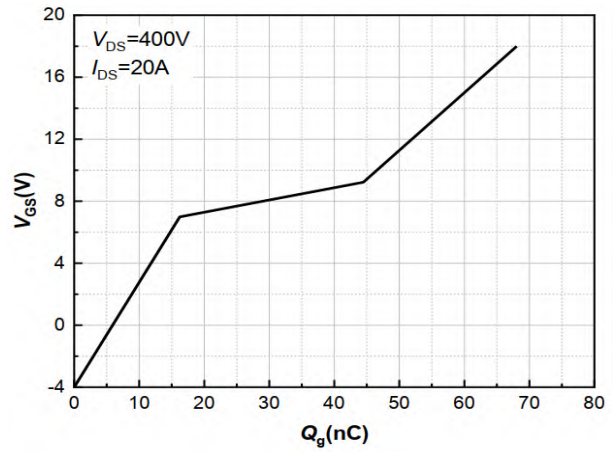


Fig 14 Gate-Charge Characteristics

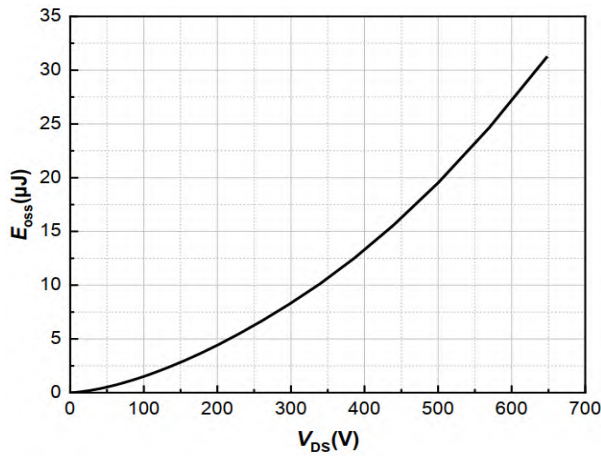


Fig 15 Output Capacitor Stored Energy

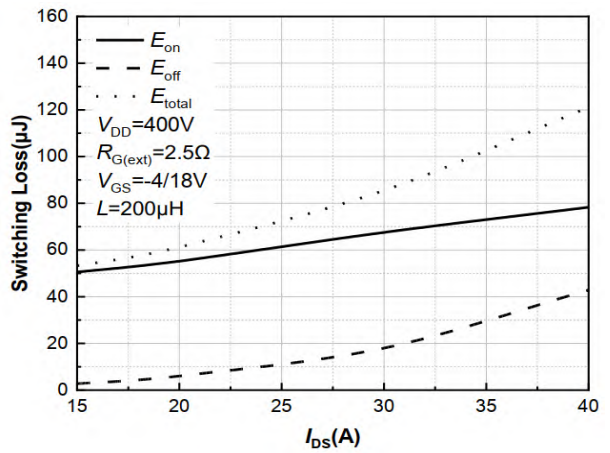


Fig 16 Clamped Inductive Switching Energy vs. Drain Current

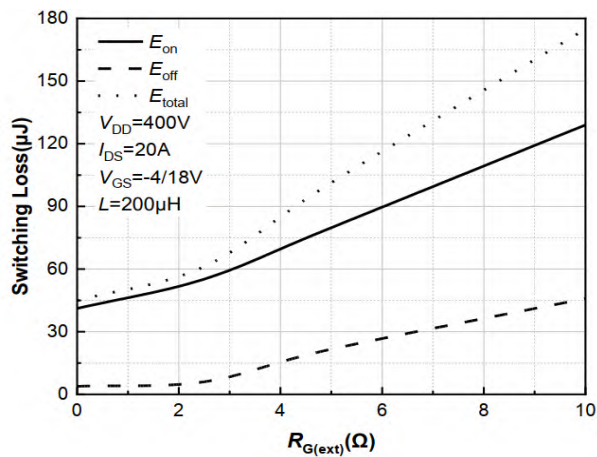


Fig 17 Clamped Inductive Switching Energy vs. R_G

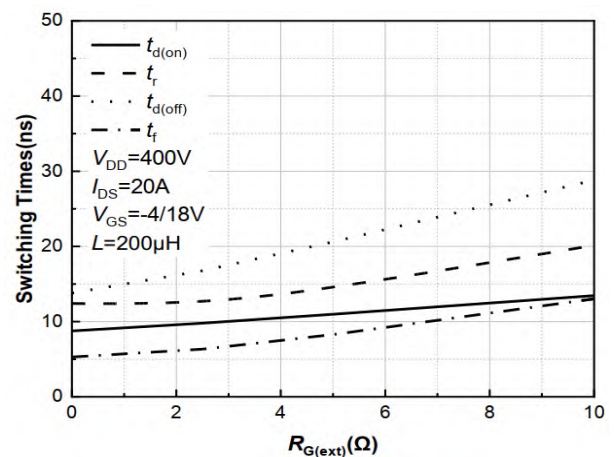


Fig 18 Switching Times vs. R_G

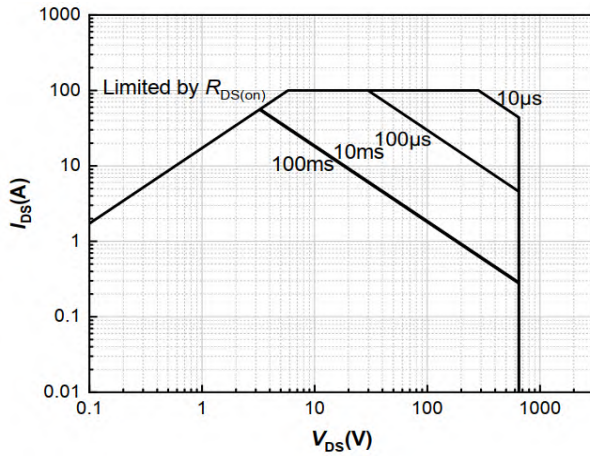


Fig 19 Safe Operating Area

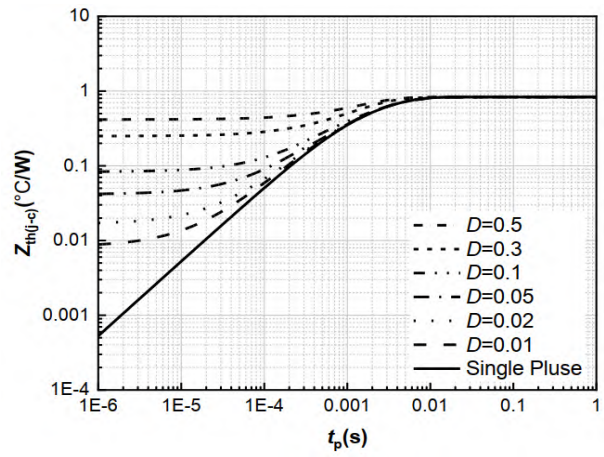


Fig 20 Maximum transient thermal impedance

Package Outline Dimensions (Unit: mm)

