

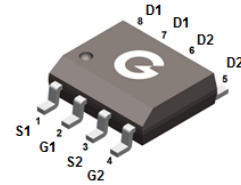
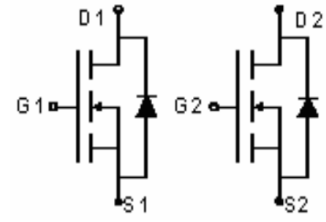
Features

- Advanced trench technology
- Low $R_{DS(on)}$
- Fast switching speed
- HBM: JESD22-A114-B: 1B
- RoHS compliant with Halogen-free

Mechanical Data

- Case: SOP-8
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208

HF



SOP-8

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
GBLN6601-S8	SOP-8	4000 pcs / Tape & Reel	Q05N06

Maximum Ratings (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DSS}	60	V
Gate-to-Source Voltage	V_{GSS}	± 20	V
Continuous Drain Current ($T_C = 25^\circ\text{C}$)	I_D	8	A
Continuous Drain Current ($T_A = 25^\circ\text{C}$) ^{*1}		5.7	
Continuous Drain Current ($T_A = 100^\circ\text{C}$) ^{*1}		3.6	
Pulsed Drain Current ($t_p = 10\mu\text{s}$, $T_A = 25^\circ\text{C}$)	I_{DM}	57	A
Single Pulse Avalanche Energy ^{*3}	E_{AS}	27	mJ
Power Dissipation ($T_A = 25^\circ\text{C}$, $R_{\theta JA} = 32^\circ\text{C/W}$) ^{*1}	P_D	6.3	W
Power Dissipation ($T_A = 25^\circ\text{C}$) ^{*1}		3.1	
Operating Junction Temperature Range	T_J	-55 ~ +150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55 ~ +150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance Junction-to-Case	$R_{\theta JC}$	-	-	20	$^\circ\text{C/W}$
Thermal Resistance Junction-to-Air ^{*1}	$R_{\theta JA}$	-	-	40	$^\circ\text{C/W}$

Electrical Characteristics (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
V _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	60	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 60V, V _{GS} = 0V	-	-	1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} = ±20V, V _{DS} = 0V	-	-	±100	nA
On Characteristics						
R _{DS(ON)}	Drain-Source On-resistance *2	V _{GS} = 10V, I _D = 5A	-	22	44	mΩ
		V _{GS} = 4.5V, I _D = 4A	-	26	49	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	1	1.5	2.5	V
R _G	Gate Resistance	V _{GS} = 0V, f = 1MHz	-	3.5	-	Ω
Dynamic Characteristics						
C _{ISS}	Input Capacitance	V _{GS} = 0V V _{DS} = 30V f = 1.0MHz	-	990	-	pF
C _{OSS}	Output Capacitance		-	63	-	
C _{RSS}	Reverse Transfer Capacitance		-	54	-	
Switching Characteristics						
t _{d(ON)}	Turn-on Delay Time	V _{DD} = 30V V _{GS} = 15V R _G = 3.3Ω I _D = 20A	-	4	-	ns
t _r	Turn-on Rise Time		-	67	-	
t _{d(OFF)}	Turn-Off Delay Time		-	34	-	
t _f	Turn-Off Fall Time		-	10	-	
Q _G	Total Gate-Charge	V _{DD} = 48V V _{GS} = 10V I _D = 5A	-	24.5	-	nC
Q _{GS}	Gate to Source Charge		-	3.3	-	
Q _{GD}	Gate to Drain (Miller) Charge		-	5.4	-	
Source-Drain Diode Characteristics						
V _{SD}	Diode Forward Voltage *2	I _{SD} = 5A, V _{GS} = 0V	-	0.8	1.2	V
t _{rr}	Reverse Recovery Time	I _{SD} = 20A, V _{GS} = 0V di/dt = 100A/μs	-	19	-	ns
Q _{rr}	Reverse Recovery Charge		-	11	-	nC

Notes:

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper
- The data tested by pulsed, pulse width ≤ 300μs, duty cycle ≤ 2%
- The E_{AS} data shows Max. rating. The test condition is V_{DD} = 30V, V_{GS} = 10V, L = 0.5mH

Ratings and Characteristics Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

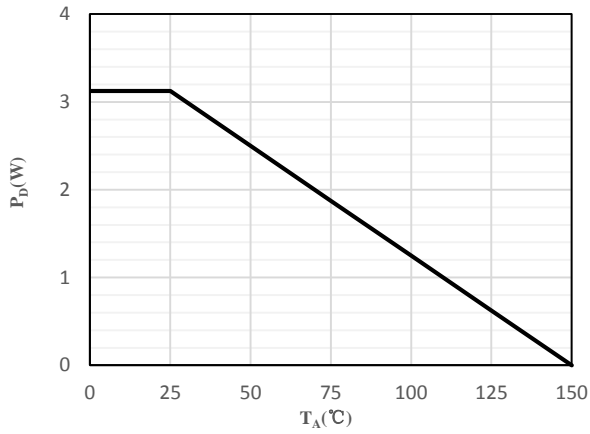


Fig 1 Power Dissipation

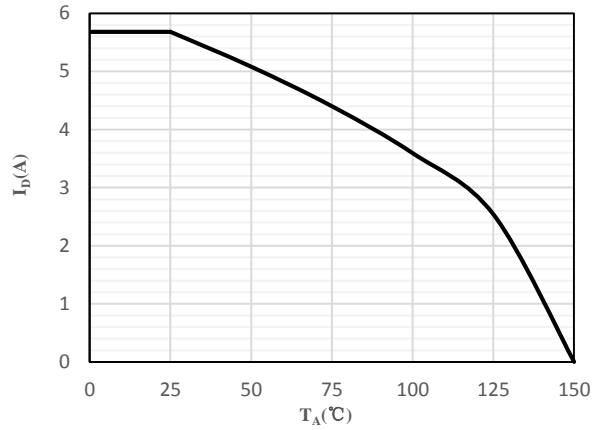


Fig 2 Drain Current

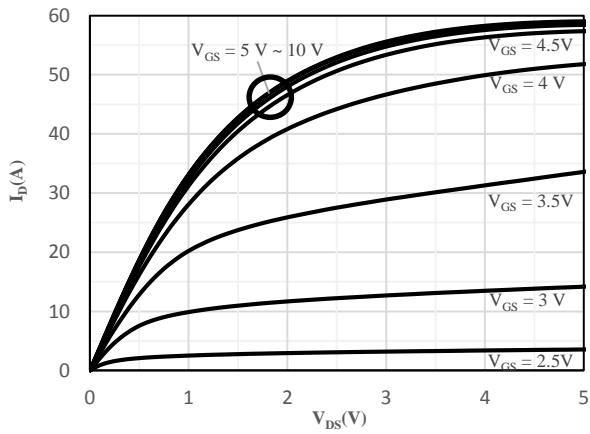


Fig 3 Typical Output Characteristics

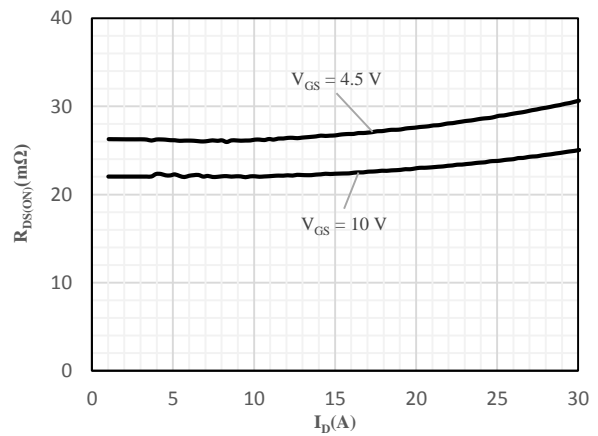


Fig 4 On-Resistance vs. Drain Current and Gate Voltage

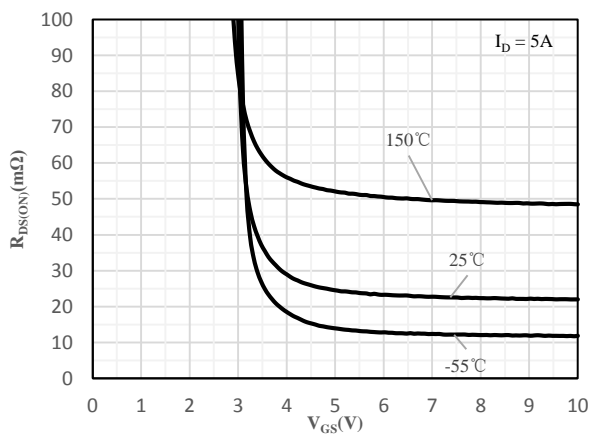


Fig 5 On-Resistance vs. Gate-Source Voltage

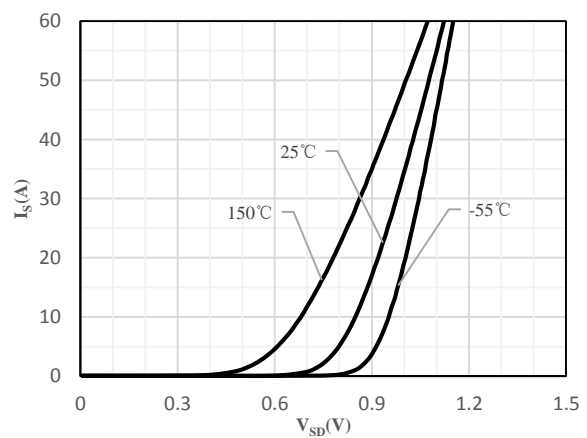


Fig 6 Body-Diode Characteristics

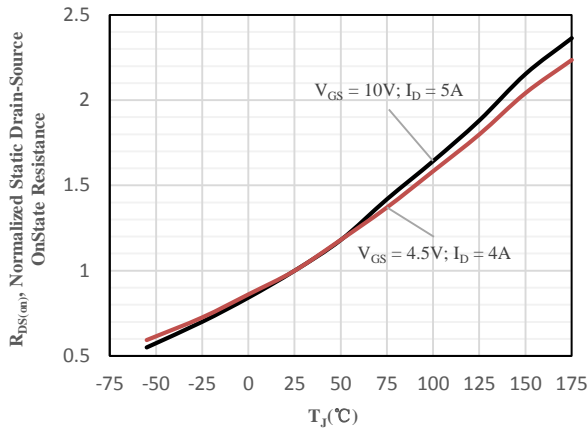


Fig 7 Normalized On-Resistance vs. Junction Temperature

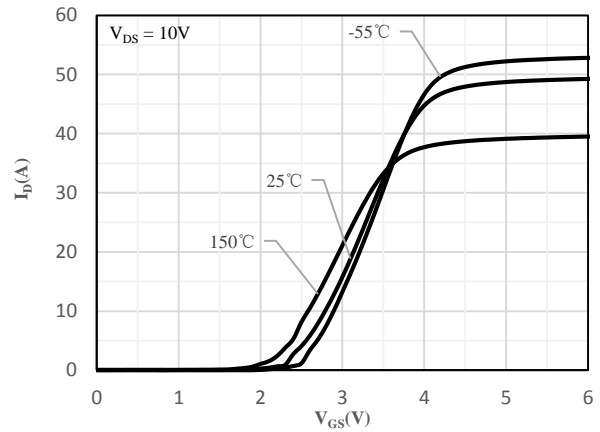


Fig 8 Transfer Characteristics

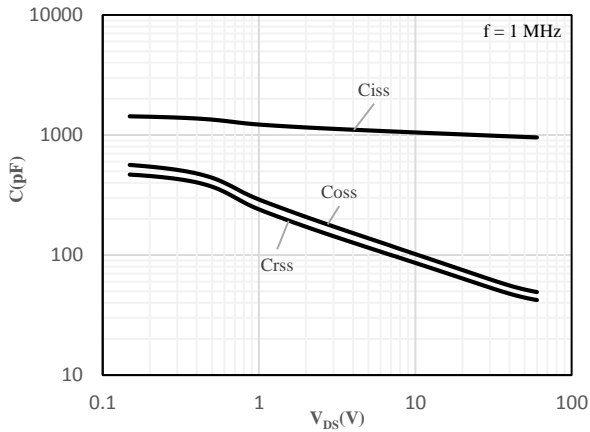


Fig 9 Capacitance Characteristics

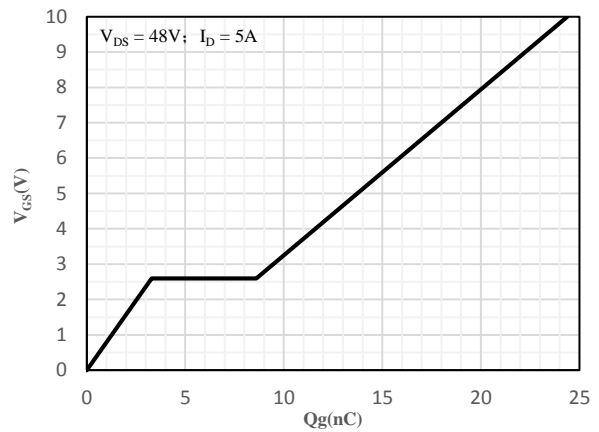


Fig 10 Gate-Charge Characteristics

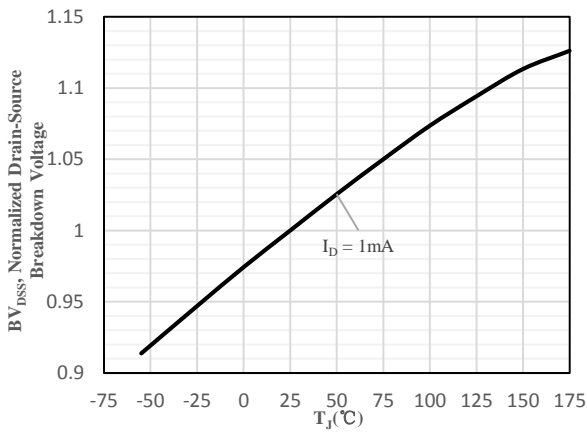


Fig 11 Normalized Breakdown Voltage vs. Junction Temperature

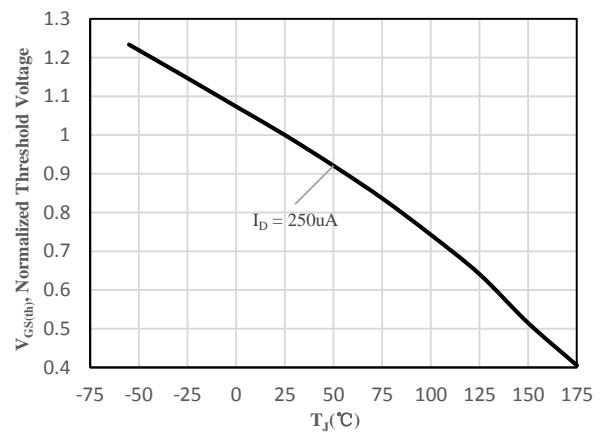


Fig 12 Normalized $V_{GS(th)}$ vs. Junction Temperature

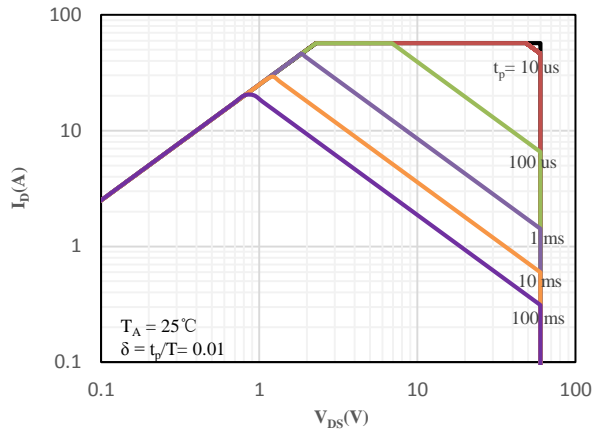


Fig 13 Safe Operating Area

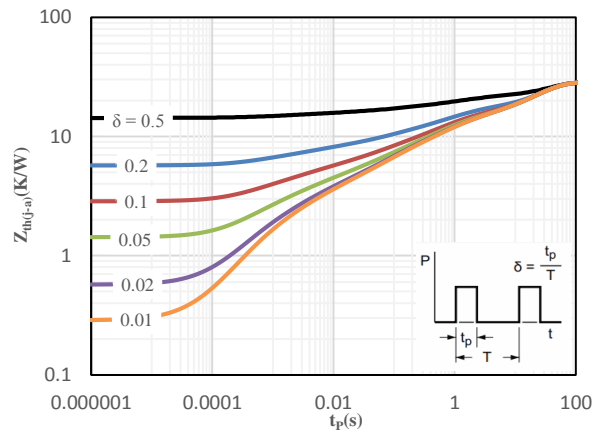
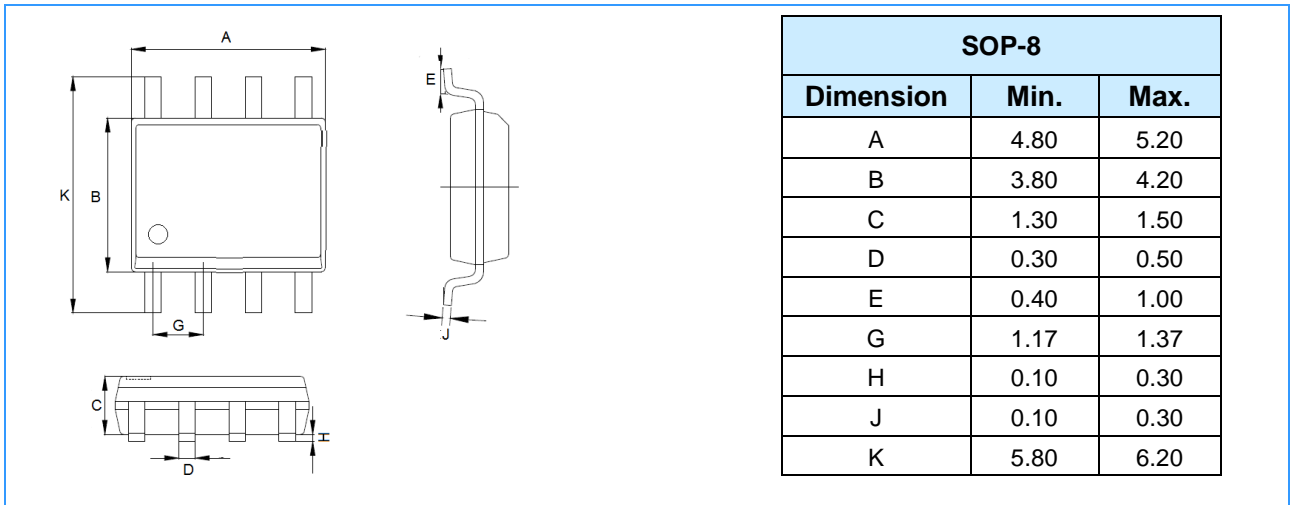


Fig 14 Maximum transient thermal impedance

Package Outline Dimensions (Unit: mm)



Mounting Pad Layout (Unit: mm)

