Dual N-Channel Enhancement Mode MOSFET

GBLN4402-3DL8

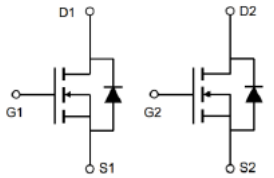
Features

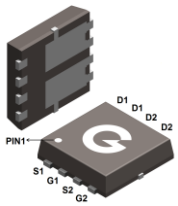
- Advanced Shielded-Gate Trench technology
- Low on-resistance
- Fast switching speed
- Low thermal resistance
- RoHS compliant with Halogen-free

HF

Mechanical Data

- Case: PDFN3×3-8LC
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208





PDFN3×3-8LC

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
GBLN4402-3DL8	PDFN3×3-8LC	5000 pcs / Tape & Reel	GBLN4402

Maximum Ratings (@ T_C = 25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DSS}	40	V
Gate-to-Source Voltage	V_{GSS}	±20	V
Continuous Drain Current (T_C = 25°C)	I_D	31	A
Continuous Drain Current (T_C = 100°C)		20	
Continuous Drain Current (T_A = 25°C) *1		12.5	
Continuous Drain Current (T_A = 100°C) *1		8	
Pulsed Drain Current (t_p =10μs, T_C = 25°C)	I_{DM}	110	A
Single Pulse Avalanche Energy *3	E_{AS}	13	mJ
Power Dissipation (T_C = 25°C)	P_D	14.7	W
Operating Junction Temperature Range	T_J	-55 ~ +150	°C
Storage Temperature Range	T_{STG}	-55 ~ +150	°C

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance Junction-to-Case	$R_{\theta JC}$	-	7.2	8.5	°C/W
Thermal Resistance Junction-to-Air *1	$R_{\theta JA}$	-	46	53	°C/W

Electrical Characteristics (@ T_A = 25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
V_{DSS}	Drain-Source Breakdown Voltage	V_{GS} = 0V, I_D = 250μA	40	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 40V, V_{GS} = 0V	-	-	1	μA
I_{GSS}	Gate-Body Leakage Current	V_{GS} = ±20V, V_{DS} = 0V	-	-	±100	nA
On Characteristics						
$R_{DS(ON)}$	Drain-Source On-resistance *2	V_{GS} = 10V,I_D = 12A	-	10	12	mΩ
		V_{GS} = 4.5V,I_D = 10A	-	14.5	16	
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250μA	1	1.7	2.5	V
R_G	Gate Resistance	V_{GS} = 0V, f = 1MHz	-	3.6	-	Ω
Dynamic Characteristics						
C_{ISS}	Input Capacitance	V_{GS} = 0V V_{DS} = 15V f = 1.0MHz	-	694	-	pF
C_{OSS}	Output Capacitance		-	150	-	
C_{RSS}	Reverse Transfer Capacitance		-	140	-	
Switching Characteristics						
$t_{d(ON)}$	Turn-on Delay Time	V_{DD} = 30V V_{GS} = 15V I_D = 20A R_G = 3.3Ω	-	11	-	ns
t_r	Turn-on Rise Time		-	61	-	
$t_{d(OFF)}$	Turn-Off Delay Time		-	30	-	
t_f	Turn-Off Fall Time		-	13	-	
Q_G	Total Gate-Charge	V_{DD} = 20V V_{GS} = 4.5V I_D = 12A	-	9.8	-	nC
Q_{GS}	Gate to Source Charge		-	1.8	-	
Q_{GD}	Gate to Drain (Miller) Charge		-	5.8	-	
Source-Drain Diode Characteristics						
V_{SD}	Diode Forward Voltage *2	I_{SD} = 1A, V_{GS} = 0V	-	0.7	1	V
trr	Reverse Recovery Time	I_{SD} = 15A, V_{GS} = 0V di/dt = 100A/μs	-	21	-	ns
Qrr	Reverse Recovery Charge		-	8.3	-	nC

Notes:

1. The data tested by surface mounted on a 1 inch^2 FR-4 board with 2OZ copper
2. The data tested by pulsed, pulse width ≤ 300μs, duty cycle ≤ 2%
3. The E_{AS} data shows Max. rating. The test condition is V_{DD} = 25V, V_{GS} = 10V, L = 0.1mH

Ratings and Characteristics Curves (@ T_A = 25°C unless otherwise specified)

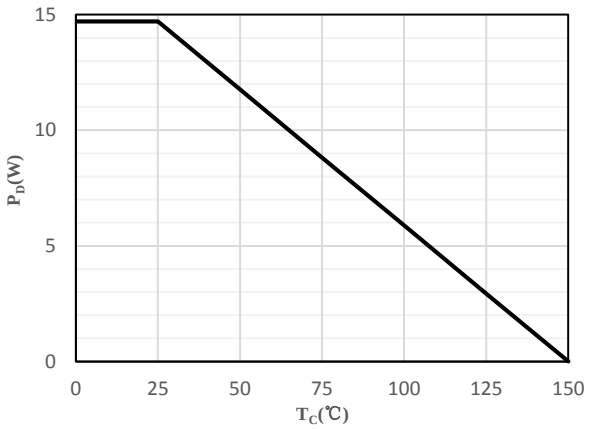


Fig 1 Power Dissipation

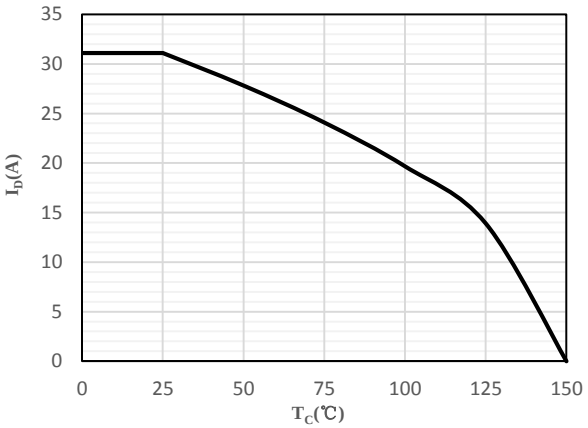


Fig 2 Drain Current

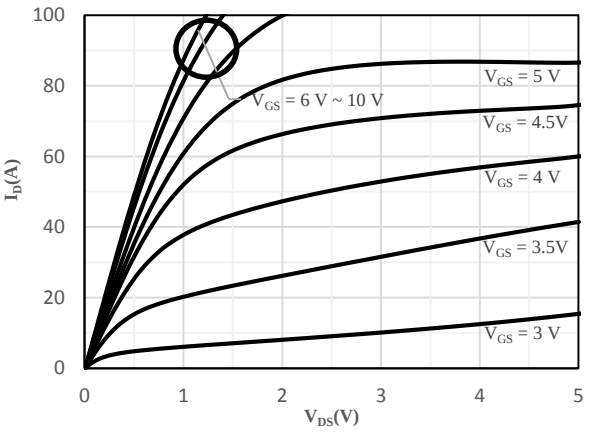


Fig 3 Typical Output Characteristics

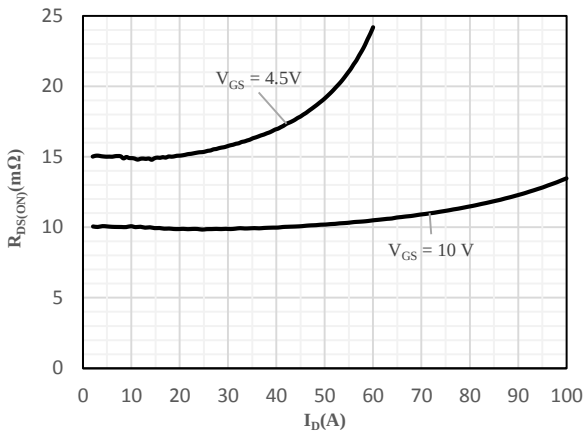


Fig 4 On-Resistance vs. Drain Current and Gate Voltage

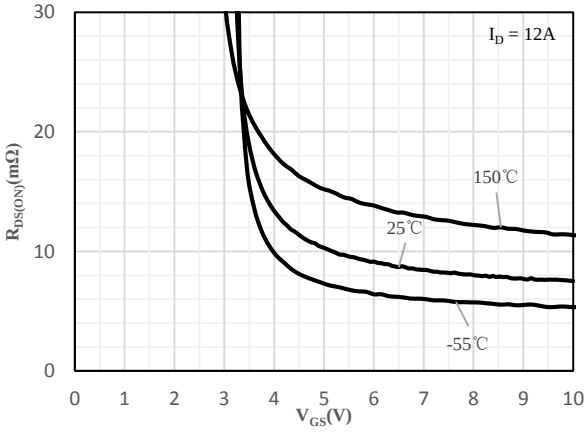


Fig 5 On-Resistance vs. Gate-Source Voltage

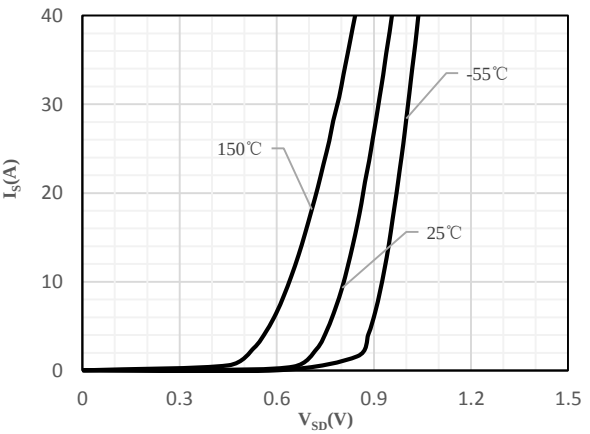


Fig 6 Body-Diode Characteristics

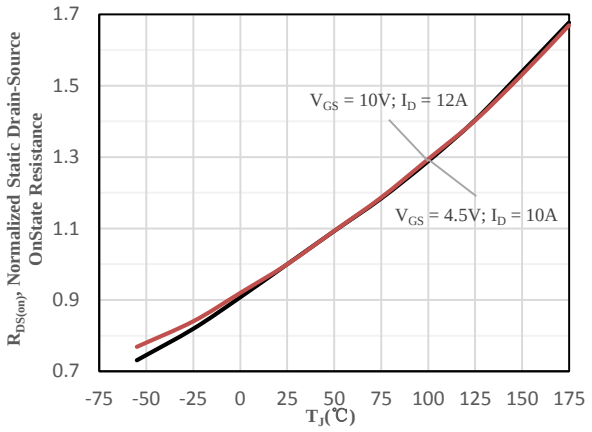


Fig 7 Normalized On-Resistance vs. Junction Temperature

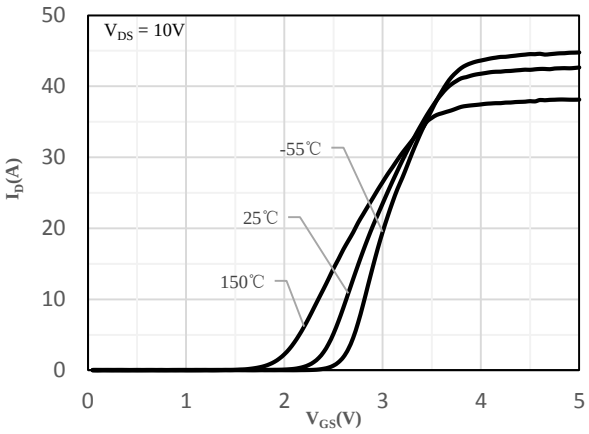


Fig 8 Transfer Characteristics

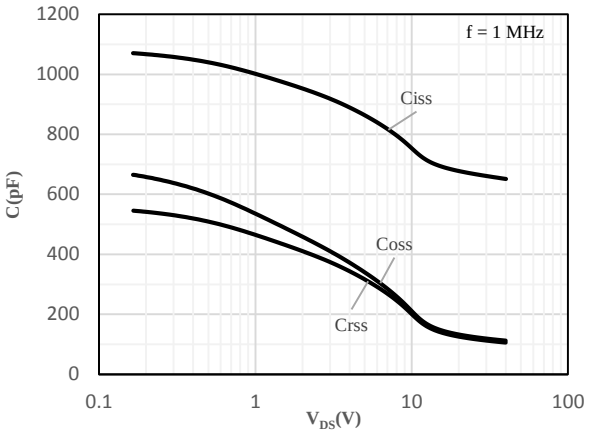


Fig 9 Capacitance Characteristics

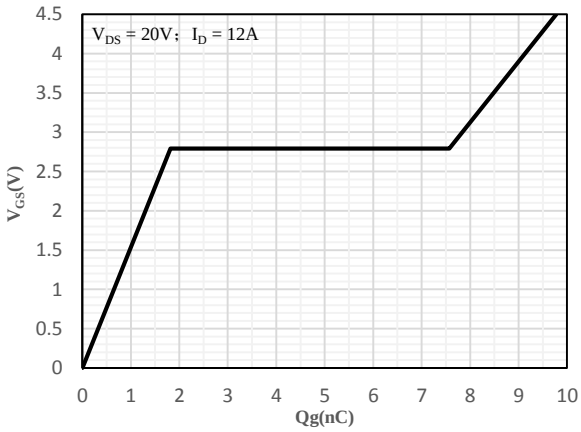


Fig 10 Gate-Charge Characteristics

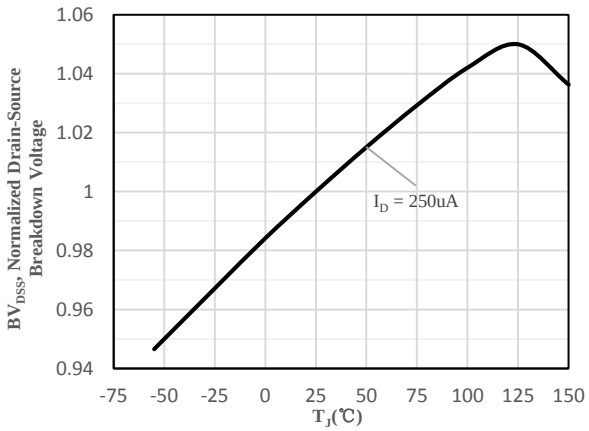


Fig 11 Normalized Breakdown Voltage vs. Junction Temperature

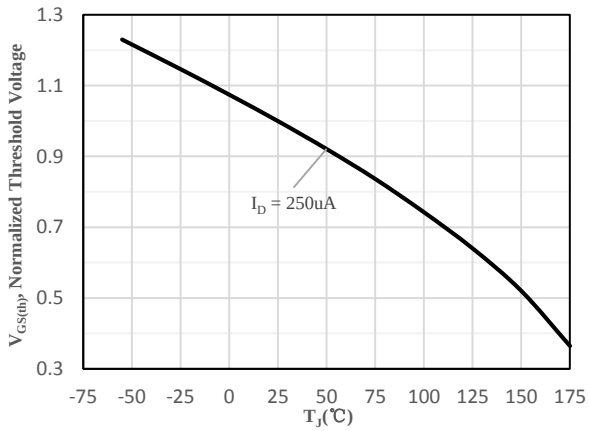


Fig 12 Normalized $V_{GS(th)}$ vs. Junction Temperature

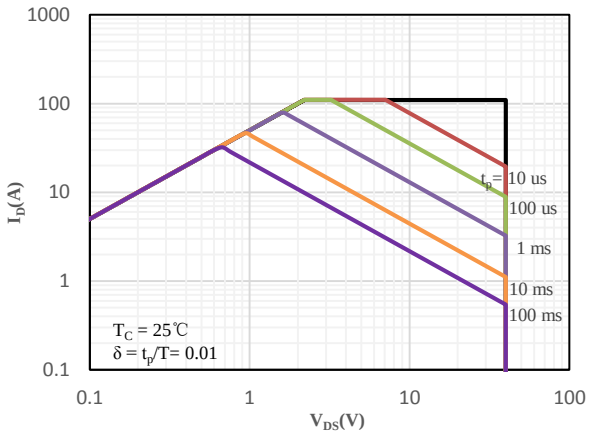


Fig 13 Safe Operating Area

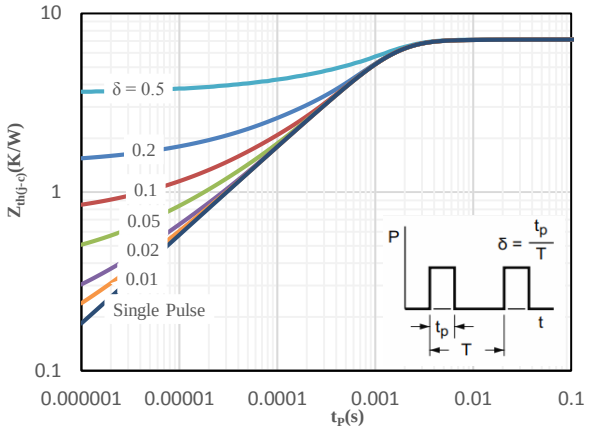
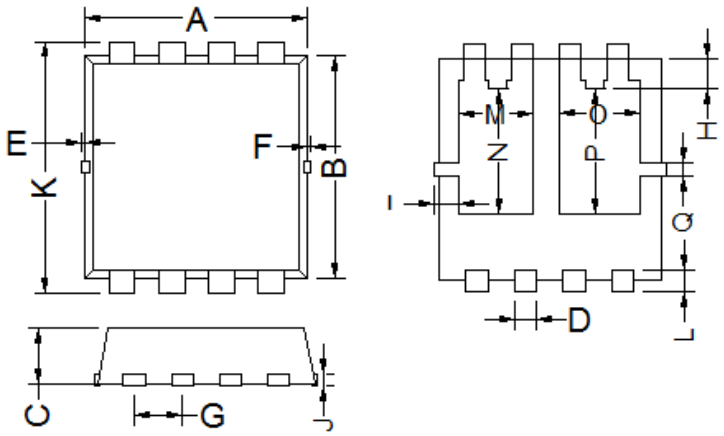


Fig 14 Maximum transient thermal impedance

Package Outline Dimensions (Unit: mm)



PDFN3×3-8LC		
Dimension	**Min.**	**Max.**
A	2.90	3.10
B	2.90	3.10
C	0.55	0.75
D	0.20	0.40
E	0.00	0.20
F	0.00	0.20
G	0.55	0.75
H	0.20	0.40
I	0.20	0.40
J	0.10	0.20
K	3.15	3.45
L	0.25	0.45
M/O	0.90	1.20
N/P	1.65	1.85
Q	0.10	0.30

Mounting Pad Layout (Unit: mm)

PDFN3×3-8LC