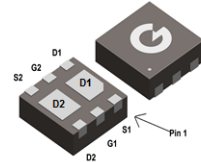
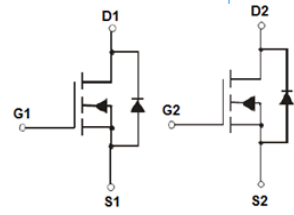


Features

- Advanced trench technology
- Low on-resistance
- Fast switching speed
- RoHS compliant with Halogen-free

HF



DFN2020-6LC

Mechanical Data

- Case: DFN2020-6LC
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
GBLN3303DF2	DFN2020-6LC	3000 pcs / Tape & Reel	N3303

Maximum Ratings (@ T_A = 25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V _{DSS}	30	V
Gate-to-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current (T _C = 25°C)	I _D	10	A
Continuous Drain Current (T _A = 25°C) *1		6.4	A
Continuous Drain Current (T _A = 70°C) *1		5.1	A
Pulsed Drain Current (t _p = 10μs, T _A = 25°C)	I _{DM}	30	A
Single Pulse Avalanche Energy *3	E _{AS}	25	mJ
Power Dissipation (T _C = 25°C)	P _D	5	W
Power Dissipation (T _A = 25°C) *1		2	W
Operating Junction Temperature Range	T _J	-55 ~ +150	°C
Storage Temperature Range	T _{STG}	-55 ~ +150	°C

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance Junction-to-Case	R _{θJC}	-	-	25	°C/W
Thermal Resistance Junction-to-Air *1	R _{θJA}	-	-	62	°C/W

Electrical Characteristics (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
V_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 24V, V_{GS} = 0V$	-	-	1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	± 100	nA
On Characteristics						
$R_{DS(ON)}$	Drain-Source On-resistance ^{*2}	$V_{GS} = 10V, I_D = 6.3A$	-	18	30	m Ω
		$V_{GS} = 4.5V, I_D = 4.8A$	-	25	45	
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	1	1.6	2.5	V
R_G	Gate Resistance	$V_{GS} = 0V, f = 1MHz$	-	8.4	-	Ω
Dynamic Characteristics						
C_{ISS}	Input Capacitance	$V_{GS} = 0V$ $V_{DS} = 15V$ $f = 1.0MHz$	-	492	-	pF
C_{OSS}	Output Capacitance		-	64	-	
C_{RSS}	Reverse Transfer Capacitance		-	59	-	
Switching Characteristics						
$t_{d(ON)}$	Turn-on Delay Time ^{*4}	$V_{DD} = 15V$ $V_{GS} = 10V$ $R_G = 3\Omega$ $R_L = 2.6\Omega$	-	4.5	-	ns
t_r	Turn-on Rise Time ^{*4}		-	2.4	-	
$t_{d(OFF)}$	Turn-Off Delay Time ^{*4}		-	14.8	-	
t_f	Turn-Off Fall Time ^{*4}		-	2.5	-	
Q_G	Total Gate-Charge	$V_{DS} = 15V$ $V_{GS} = 10V$ $I_D = 5.8A$	-	14.6	-	nC
Q_{GS}	Gate to Source Charge		-	2.4	-	
Q_{GD}	Gate to Drain (Miller) Charge		-	2.9	-	
Source-Drain Diode Characteristics						
V_{SD}	Diode Forward Voltage ^{*2}	$I_S = 6A, V_{GS} = 0V$	-	0.9	1.2	V
t_{rr}	Reverse Recovery Time	$I_{SD} = 5A, V_{GS} = 0V$ $di/dt = 100A/\mu s$	-	75	-	ns
Q_{rr}	Reverse Recovery Charge		-	36	-	nC

Notes:

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper
2. The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
3. The E_{AS} data shows Max. rating. The test condition is $V_{DD} = 25V, V_{GS} = 10V, L = 10mH$
4. Guaranteed by design, not subject to production

Ratings and Characteristics Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

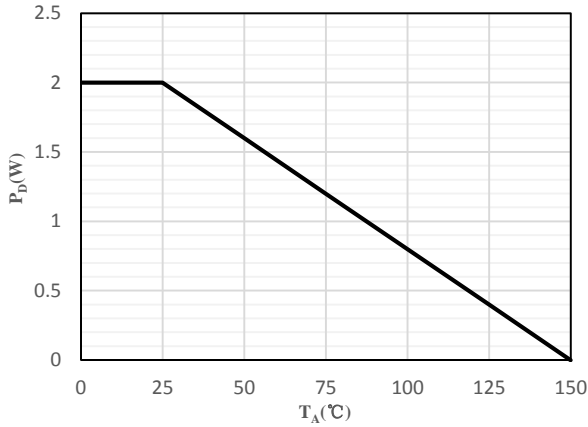


Fig 1 Power Dissipation

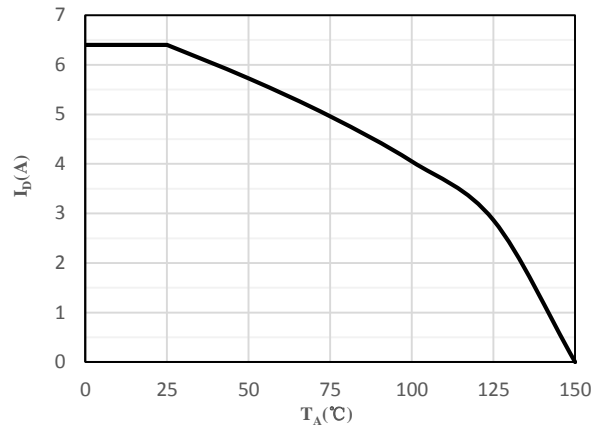


Fig 2 Drain Current

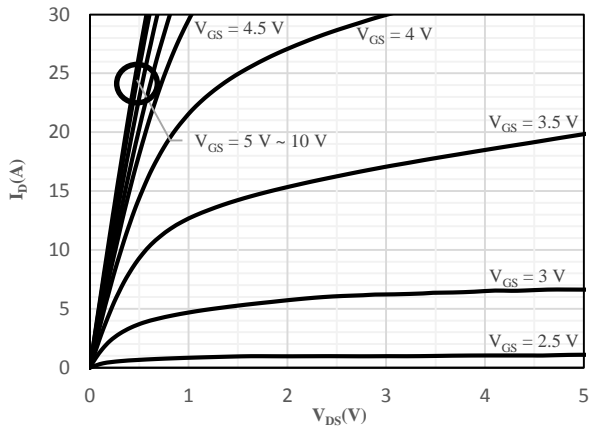


Fig 3 Typical Output Characteristics

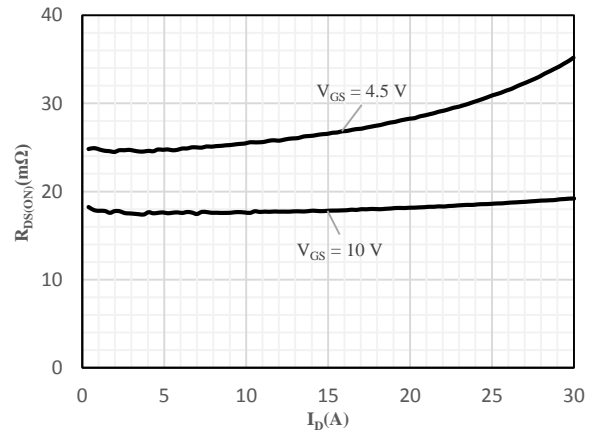


Fig 4 On-Resistance vs. Drain Current and Gate Voltage

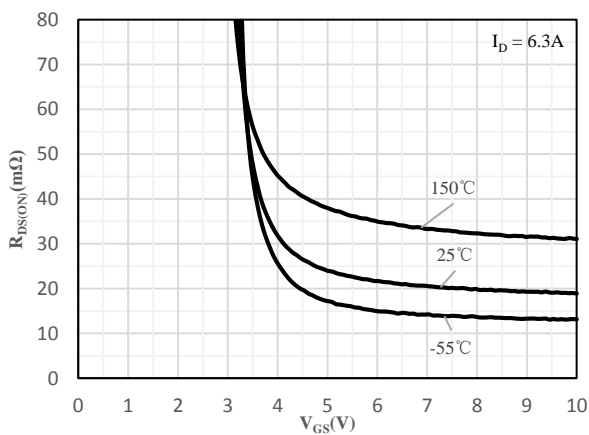


Fig 5 On-Resistance vs. Gate-Source Voltage

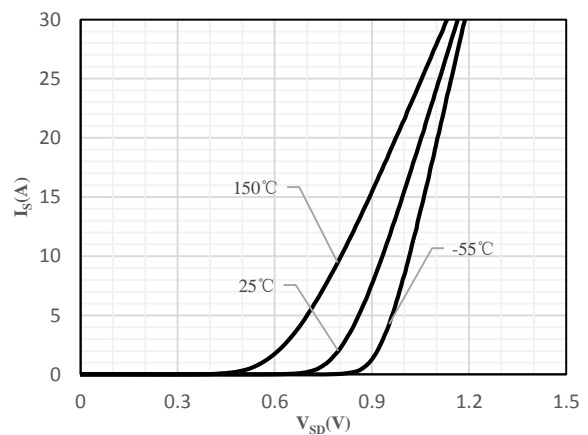


Fig 6 Body-Diode Characteristics

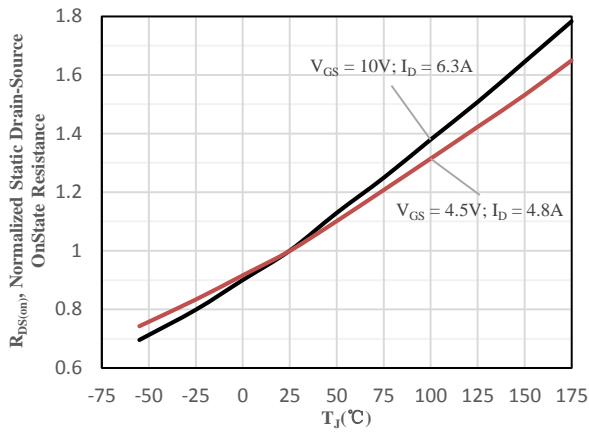


Fig 7 Normalized On-Resistance vs. Junction Temperature

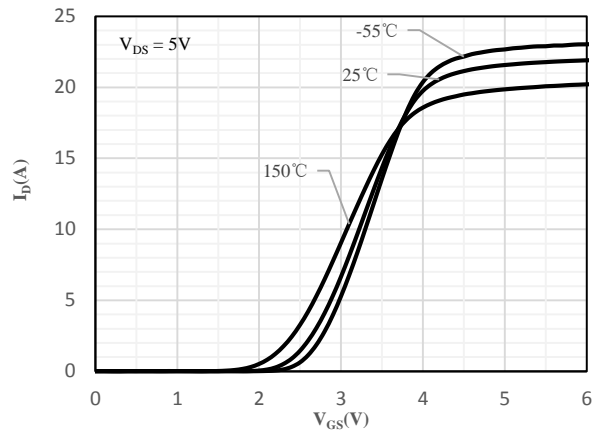


Fig 8 Transfer Characteristics

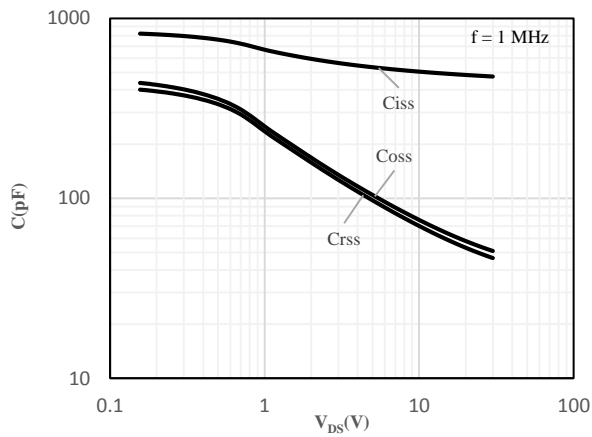


Fig 9 Capacitance Characteristics

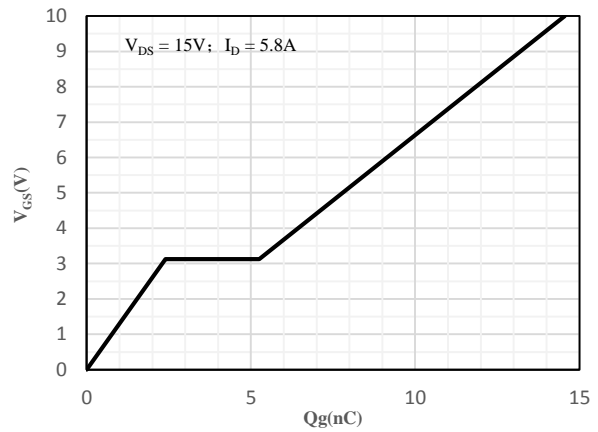


Fig 10 Gate-Charge Characteristics

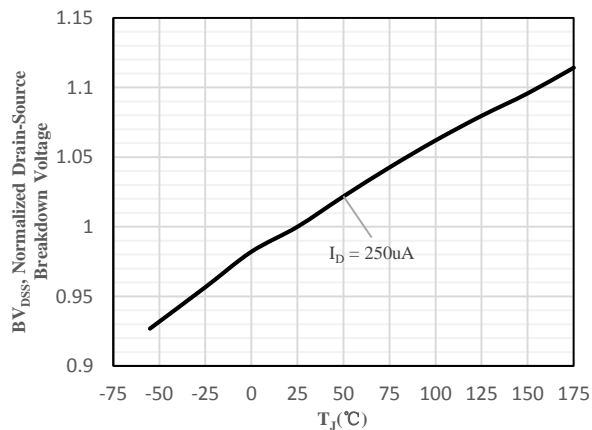


Fig 11 Normalized Breakdown Voltage vs. Junction Temperature

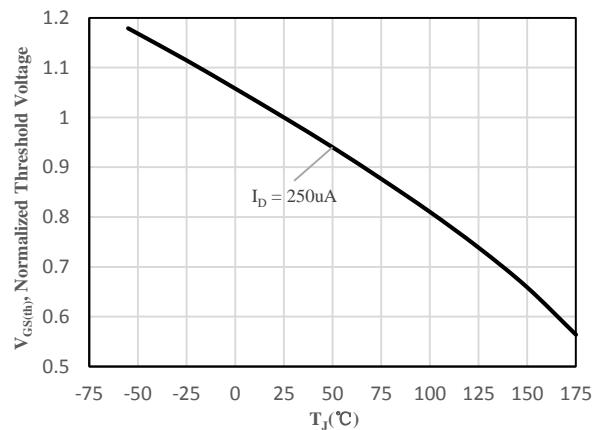
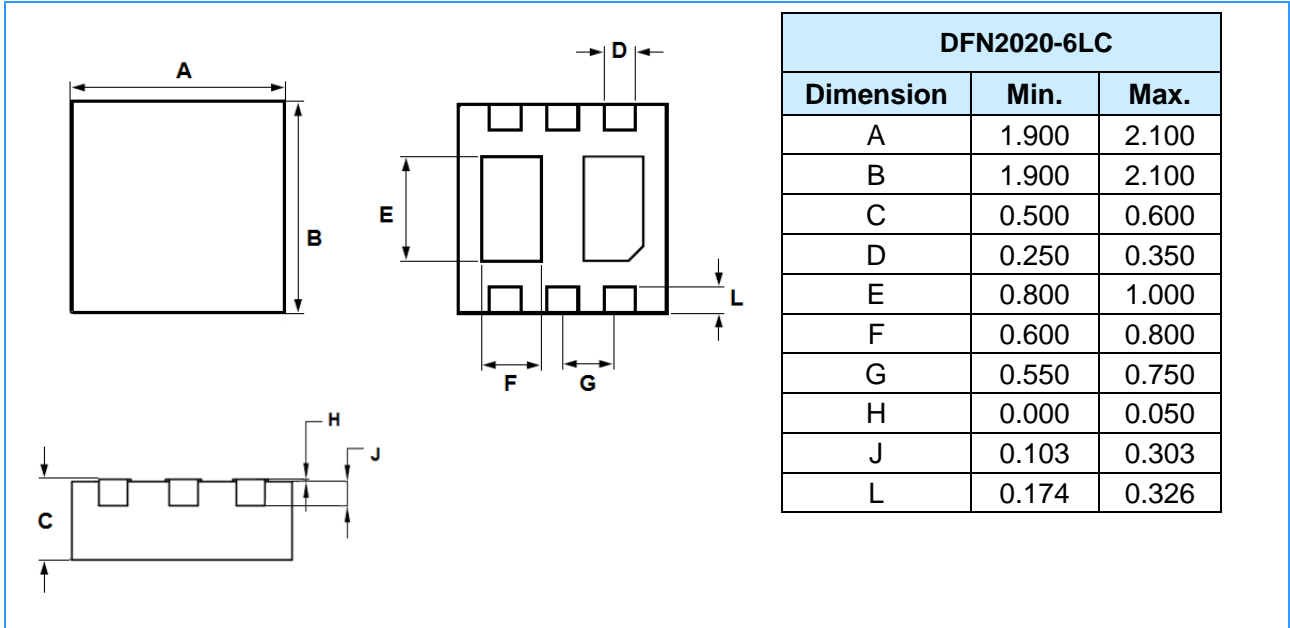


Fig 12 Normalized $V_{GS(th)}$ vs. Junction Temperature

Package Outline Dimensions (Unit: mm)



Mounting Pad Layout (Unit: mm)

