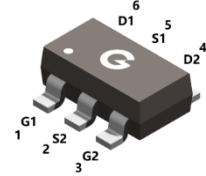
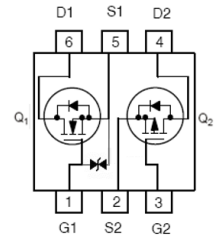


Features

- Advanced trench technology
- Custom-designed for clients' special needs
- Reliable and Rugged
- Green device available
- RoHS compliant with Halogen-free

HF



SOT-23-6L

Mechanical Data

- Case: SOT-23-6L
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
GBLH5301-6L	SOT-23-6L	3000 pcs / Tape & Reel	H5301

Maximum Ratings (@ T_A = 25°C unless otherwise specified)

Parameter	Symbol	N	P	Unit
Drain-to-Source Voltage	V _{DSS}	50	-30	V
Gate-to-Source Voltage	V _{GSS}	±20	±20	V
Continuous Drain Current (T _A = 25°C) *1	I _D	0.36	-4.3	A
Continuous Drain Current (T _A = 70°C) *1		0.29	-3.5	A
Pulsed Drain Current (t _p = 10μs, T _A = 25°C)	I _{DM}	1.5	-17	A
Single Pulse Avalanche Energy *3	E _{AS}	0.2	12	mJ
Power Dissipation (T _A = 25°C) *1	P _D	0.5	1	W
Operating Junction Temperature Range	T _J	-55 ~ +150		°C
Storage Temperature Range	T _{STG}	-55 ~ +150		°C

Thermal Characteristics

Parameter	Symbol	N	P	Unit
Thermal Resistance Junction-to-Air *1	R _{θJA}	250	125	°C/W

Electrical Characteristics-N (@ T_A = 25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
V _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	50	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 50V, V _{GS} = 0V	-	-	1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} = ±20V, V _{DS} = 0V	-	-	±10	μA
On Characteristics						
R _{DS(ON)}	Drain-Source On-resistance *2	V _{GS} = 10V, I _D = 0.5A	-	1	1.6	Ω
		V _{GS} = 4.5V, I _D = 0.2A	-	1.2	2.5	
		V _{GS} = 2.5V, I _D = 0.1A	-	1.7	4.5	
V _{GS(TH)}	Static Drain-Source On-resistance	V _{DS} = V _{GS} , I _D = 250μA	0.8	1	1.5	V
R _G	Gate Resistance	V _{GS} = 0V, f = 1MHz	-	48	-	Ω
Dynamic Characteristics						
C _{ISS}	Input Capacitance	V _{GS} = 0V V _{DS} = 25V f = 1.0MHz	-	32	-	pF
C _{OSS}	Output Capacitance		-	6	-	
C _{RSS}	Reverse Transfer Capacitance		-	3	-	
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time *4	V _{DD} = 25V, I _D = 0.36A V _{GS} = 10V, R _G = 6Ω	-	2.2	-	ns
t _r	Turn-on Rise Time *4		-	19.2	-	
t _{d(off)}	Turn-Off Delay Time *4		-	6.2	-	
t _f	Turn-Off Fall Time *4		-	23	-	
Q _G	Total Gate-Charge	V _{DS} = 25V V _{GS} = 10V I _D = 0.2A	-	4	-	nC
Q _{GS}	Gate to Source Charge		-	0.5	-	
Q _{GD}	Gate to Drain (Miller) Charge		-	0.4	-	
Source-Drain Diode Characteristics						
V _{SD}	Diode Forward Voltage *2	I _S = 0.5A, V _{GS} = 0V	-	0.9	1.2	V
t _{rr}	Reverse Recovery Time	I _F = 1A, V _{GS} = 0V	-	15	-	ns
Q _{rr}	Reverse Recovery Charge	dI _F /dt = 100A/μs	-	8	-	nC

Electrical Characteristics-P (@ T_A = 25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
V _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = -250μA	-30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = -24V, V _{GS} = 0V	-	-	-1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} = ±20V, V _{DS} = 0V	-	-	±100	nA
On Characteristics						
R _{DS(ON)}	Drain-Source On-resistance *2	V _{GS} = -10V, I _D = -4.1A	-	35	52	mΩ
		V _{GS} = -4.5V, I _D = -3A	-	45	87	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = -250μA	-1	-1.5	-2.1	V
R _G	Gate Resistance	V _{GS} = 0V, f = 1MHz	-	32	-	Ω
Dynamic Characteristics						
C _{ISS}	Input Capacitance	V _{GS} = 0V V _{DS} = -15V f = 1.0MHz	-	613	-	pF
C _{OSS}	Output Capacitance		-	72	-	
C _{RSS}	Reverse Transfer Capacitance		-	60	-	
Switching Characteristics						
t _{d(ON)}	Turn-on Delay Time *4	V _{DD} = -15V, V _{GS} = -10V R _G = 2.5Ω, R _L = 15Ω I _D = -1A	-	5	-	ns
t _r	Turn-on Rise Time *4		-	6	-	
t _{d(OFF)}	Turn-Off Delay Time *4		-	28	-	
t _f	Turn-Off Fall Time *4		-	7	-	
Q _G	Total Gate-Charge	V _{DD} = -20V	-	7.3	-	nC
Q _{GS}	Gate to Source Charge	V _{GS} = -4.5V	-	2.1	-	
Q _{GD}	Gate to Drain (Miller) Charge	I _D = -3A	-	2.2	-	
Source-Drain Diode Characteristics						
V _{SD}	Diode Forward Voltage *2	I _{SD} = -1A, V _{GS} = 0V	-	-0.8	-1.2	V
t _{rr}	Reverse Recovery Time	I _{SD} = -3A, V _{GS} = 0V di/dt = 100A/μs	-	125	-	ns
Q _{rr}	Reverse Recovery Charge		-	110	-	nC

Notes:

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper
- The data tested by pulsed, pulse width ≤ 300μs, duty cycle ≤ 2%
- The E_{AS} data shows Max. rating. The test condition is N: V_{DD} = 30V, V_{GS} = 10V, L = 0.5mH
P: V_{DD} = -15V, V_{GS} = -10V, L = 0.5mH
- Guaranteed by design, not subject to production

Ratings and Characteristics Curves-N (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

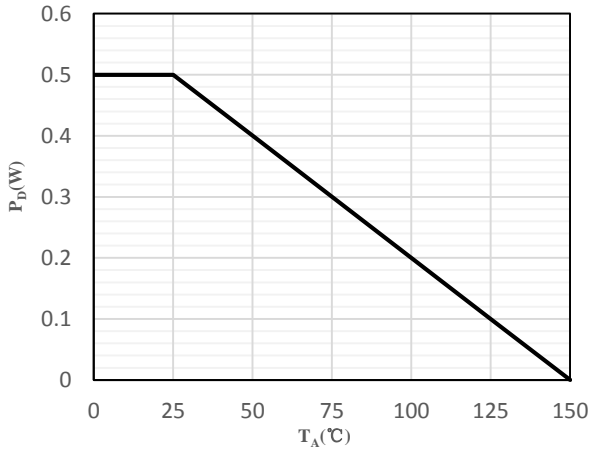


Fig 1 Power Dissipation

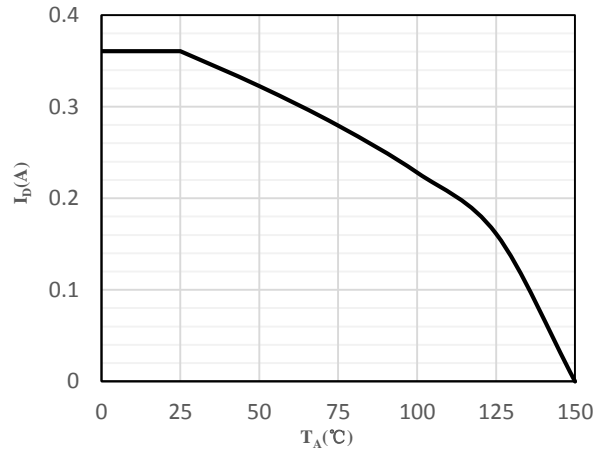


Fig 2 Drain Current

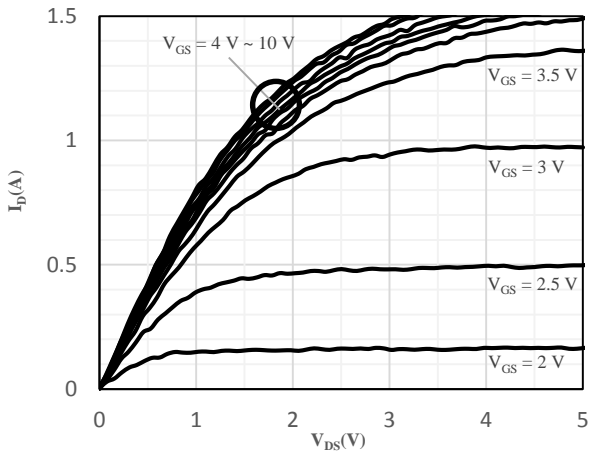


Fig 3 Typical Output Characteristics

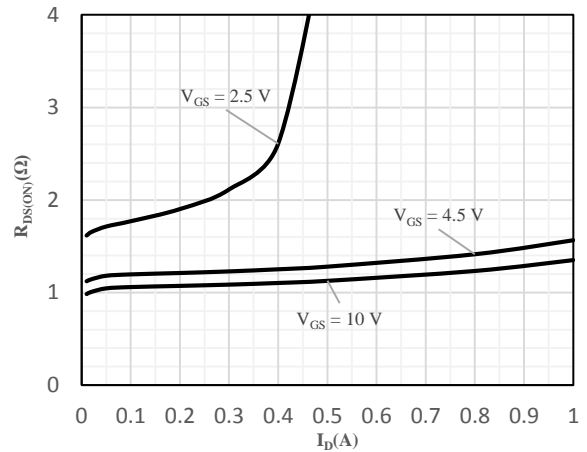


Fig 4 On-Resistance vs. Drain Current and Gate Voltage

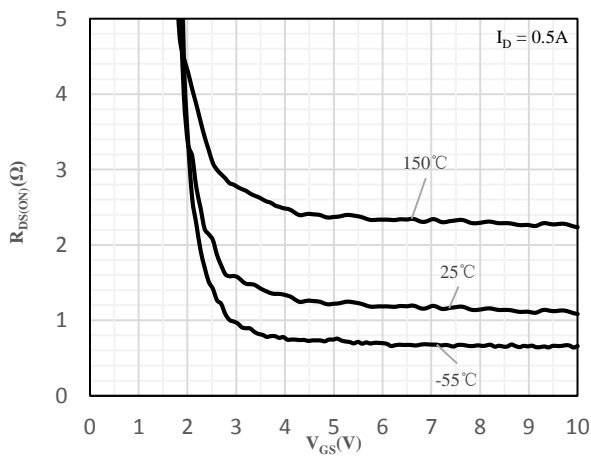


Fig 5 On-Resistance vs. Gate-Source Voltage

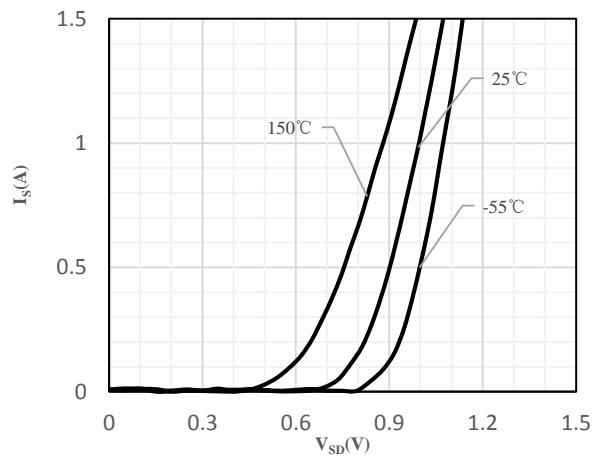


Fig 6 Body-Diode Characteristics

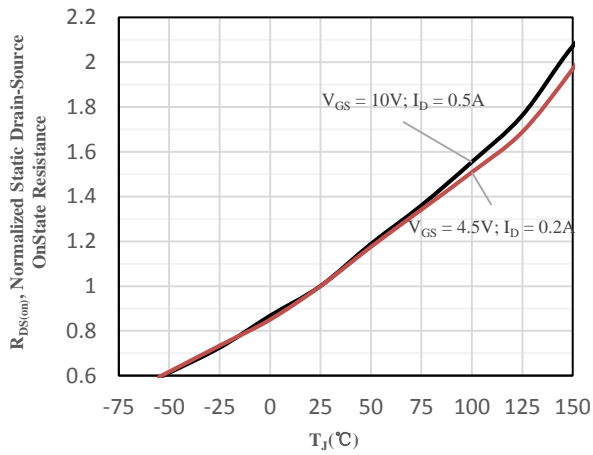


Fig 7 Normalized On-Resistance vs. Junction Temperature

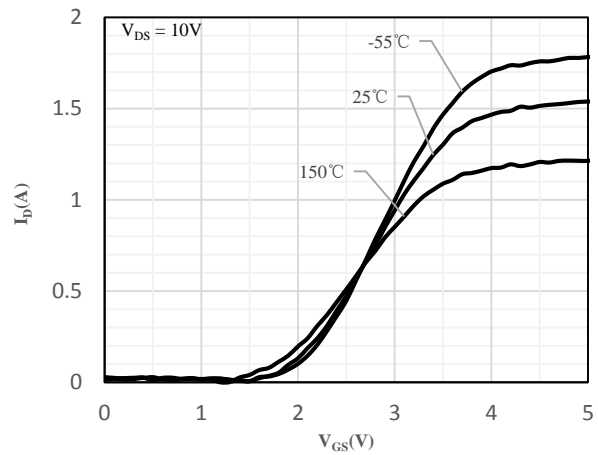


Fig 8 Transfer Characteristics

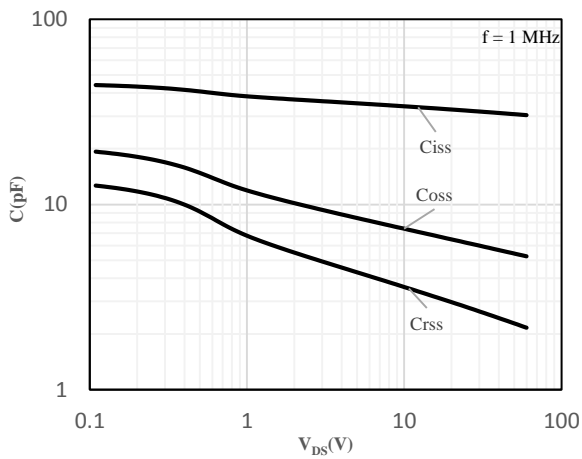


Fig 9 Capacitance Characteristics

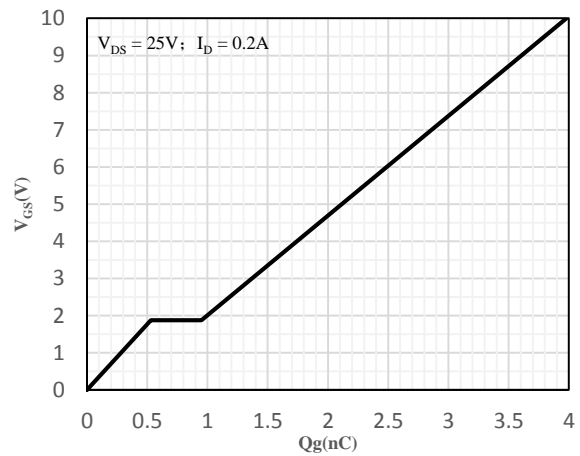


Fig 10 Gate-Charge Characteristics

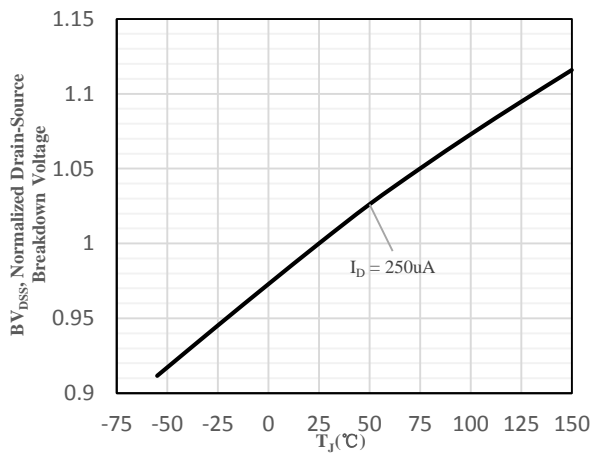


Fig 11 Normalized Breakdown Voltage vs. Junction Temperature

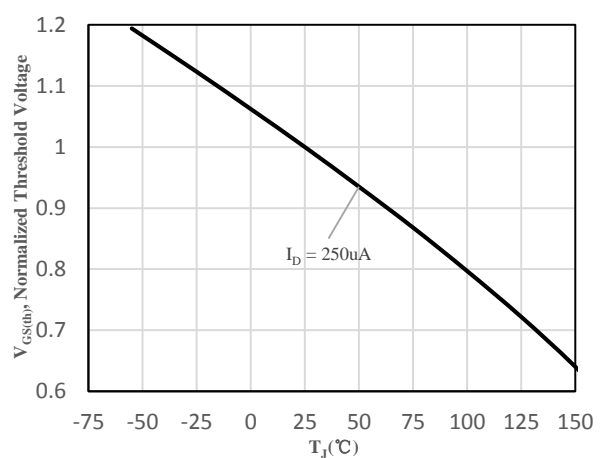


Fig 12 Normalized $V_{GS(th)}$ vs. Junction Temperature

Ratings and Characteristics Curves-P (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

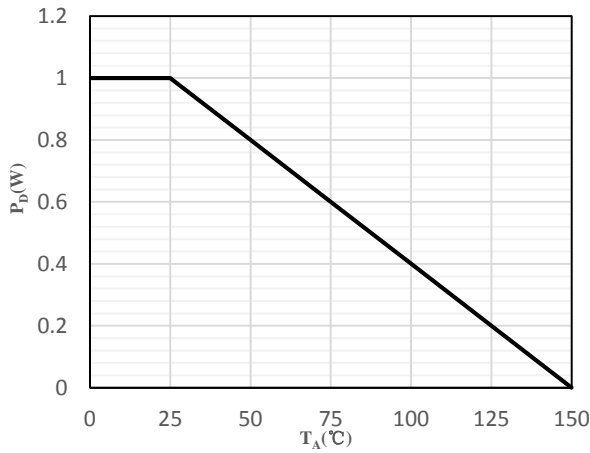


Fig 1 Power Dissipation

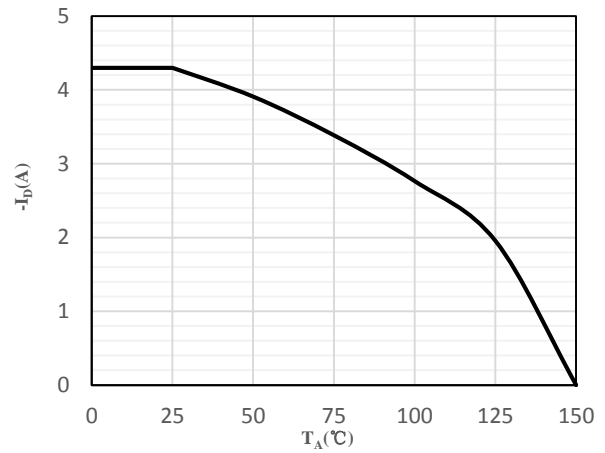


Fig 2 Drain Current

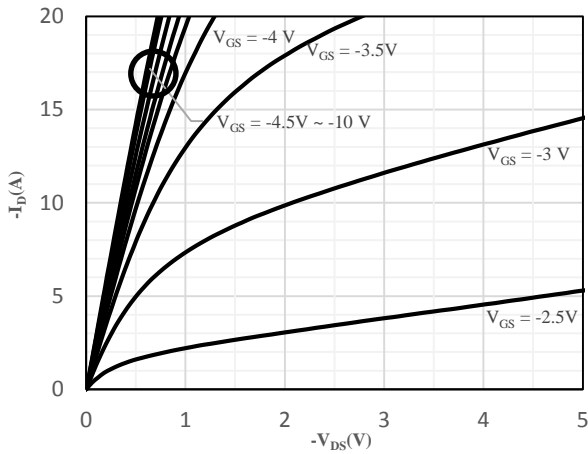


Fig 3 Typical Output Characteristics

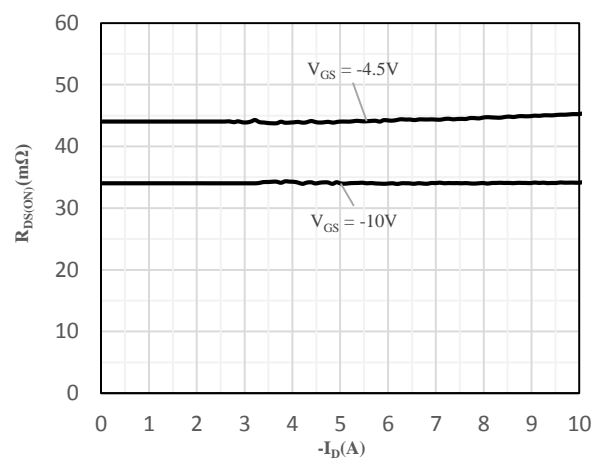


Fig 4 On-Resistance vs. Drain Current and Gate Voltage

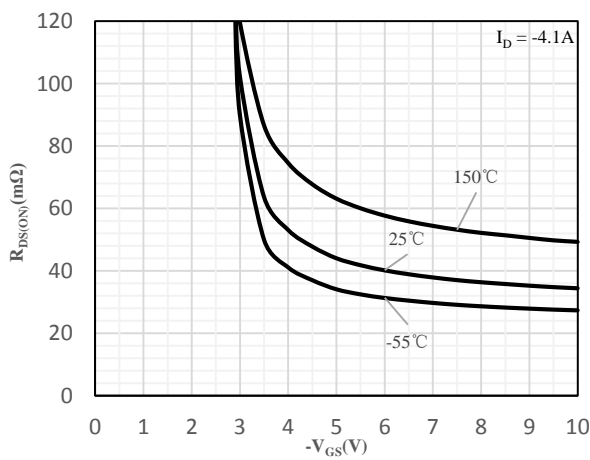


Fig 5 On-Resistance vs. Gate-Source Voltage

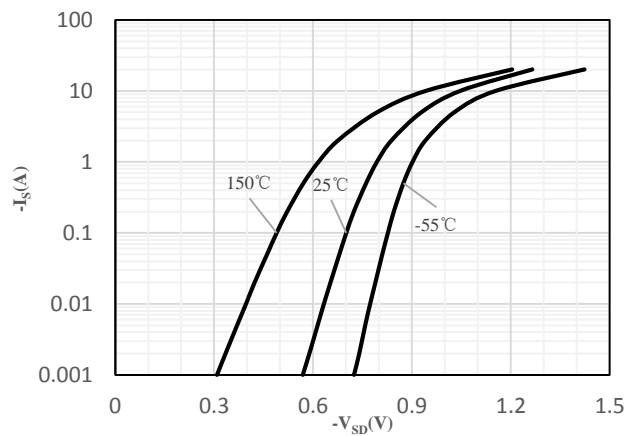


Fig 6 Body-Diode Characteristics

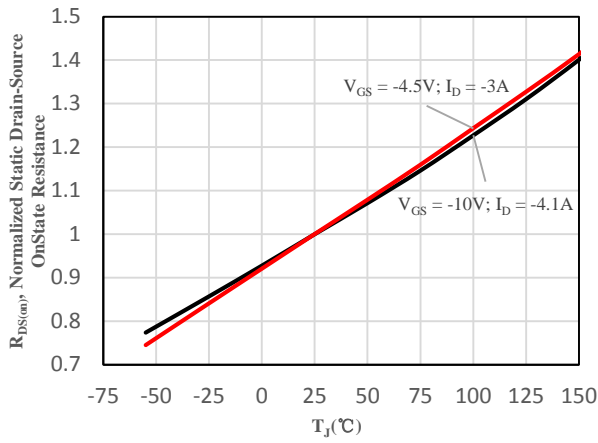


Fig 7 Normalized On-Resistance vs. Junction Temperature

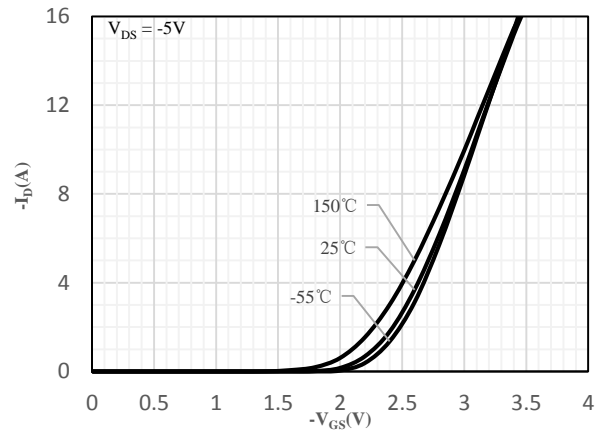


Fig 8 Transfer Characteristics

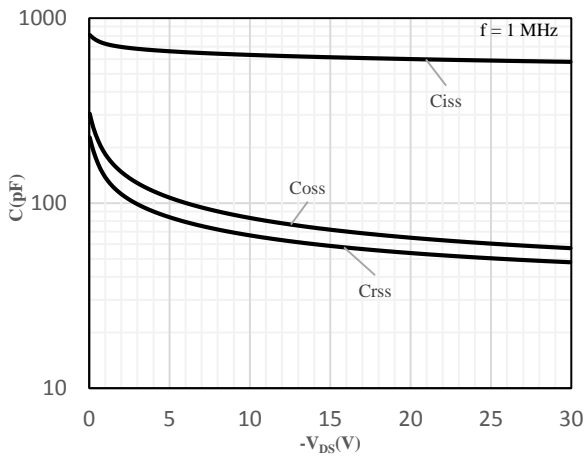


Fig 9 Capacitance Characteristics

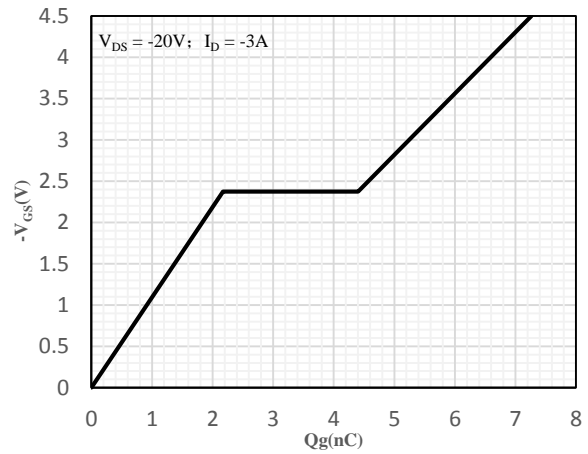


Fig 10 Gate-Charge Characteristics

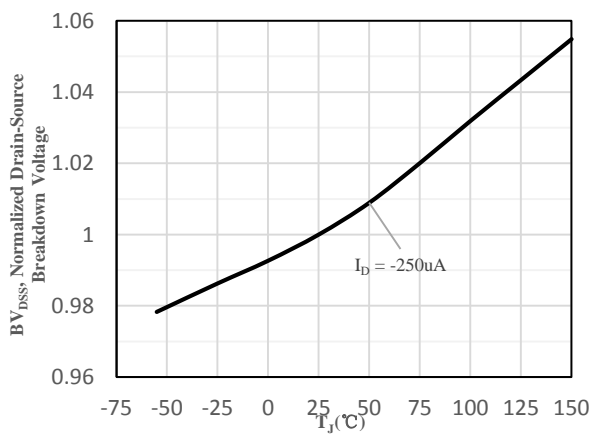


Fig 11 Normalized Breakdown Voltage vs. Junction Temperature

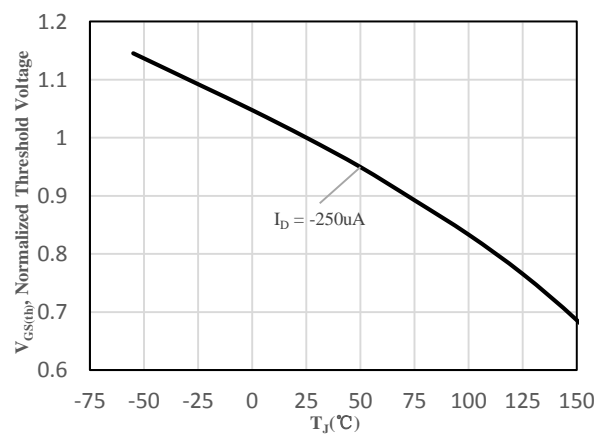
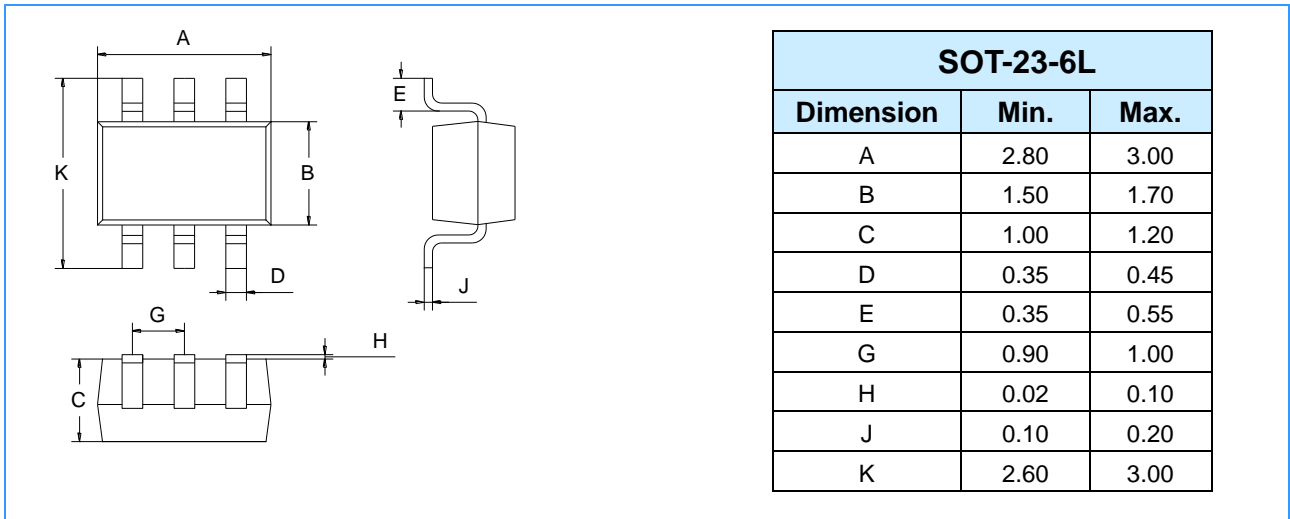


Fig 12 Normalized $V_{GS(th)}$ vs. Junction Temperature

Package Outline Dimensions (Unit: mm)



Package Outline Dimensions (Unit: mm)

