

Features

- Reliable and Rugged
- Green device available
- RoHS compliant with Halogen-free

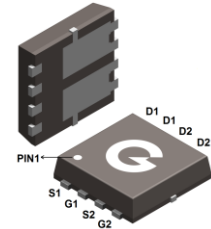
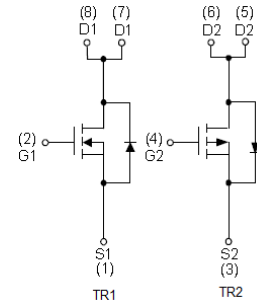
Applications

- Synchronous Rectification
- Motor Control
- Portable equipment application

Mechanical Data

- Case: PDFN3x3-8LC
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208

HF



PDFN3×3-8LC

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
GBLH3302-3DL8	PDFN3x3-8LC	5000 pcs / Tape & Reel	GBLH3302

Maximum Ratings (@ T_C = 25°C unless otherwise specified)

Parameter	Symbol	TR1	TR2	Unit
Drain-to-Source Voltage	V _{DSS}	30	-30	V
Gate-to-Source Voltage	V _{GSS}	±20	±20	V
Continuous Drain Current (T _C = 25°C)	I _D	35	-25	A
Continuous Drain Current (T _C = 100°C)		22	-16	A
Continuous Drain Current (T _A = 25°C) ^{*1}		8	-6.3	A
Continuous Drain Current (T _A = 100°C) ^{*1}		5	-4	A
Pulsed Drain Current (t _p = 10μs, T _C = 25°C)	I _{DM}	140	-100	A
Single Pulse Avalanche Energy ^{*3}	E _{AS}	16	15	mJ
Power Dissipation (T _C = 25°C)	P _D	21		W
Power Dissipation (T _A = 25°C) ^{*1}		1.25		W
Operating Junction Temperature Range	T _J	-55 ~ +150		°C
Storage Temperature Range	T _{STG}	-55 ~ +150		°C

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance Junction-to-Case	$R_{\theta JC}$	-	-	6	$^{\circ}C/W$
Thermal Resistance Junction-to-Air ^{*1}	$R_{\theta JA}$	-	-	100	$^{\circ}C/W$

Electrical Characteristics-TR1 (@ $T_A = 25^{\circ}C$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
V_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 30V, V_{GS} = 0V$	-	-	1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	± 100	nA
On Characteristics						
$R_{DS(ON)}$	Drain-Source On-resistance ^{*2}	$V_{GS} = 10V, I_D = 20A$	-	11	13	m Ω
		$V_{GS} = 4.5V, I_D = 10A$	-	16	18	m Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	1	1.5	2.5	V
R_G	Gate Resistance	$V_{GS} = 0V, f = 1MHz$	-	4	-	Ω
Dynamic Characteristics						
C_{ISS}	Input Capacitance	$V_{GS} = 0V$ $V_{DS} = 15V$ $f = 1.0MHz$	-	710	-	pF
C_{OSS}	Output Capacitance		-	106	-	
C_{RSS}	Reverse Transfer Capacitance		-	83	-	
Switching Characteristics						
$t_{d(ON)}$	Turn-on Delay Time ^{*4}	$V_{DD} = 15V$ $V_{GS} = 10V$ $I_D = 20A$ $R_G = 3\Omega$	-	3	-	ns
t_r	Turn-on Rise Time ^{*4}		-	2	-	
$t_{d(OFF)}$	Turn-Off Delay Time ^{*4}		-	13	-	
t_f	Turn-Off Fall Time ^{*4}		-	6	-	
Q_G	Total Gate-Charge	$V_{DD} = 15V$ $V_{GS} = 10V$ $I_D = 30A$	-	13.5	-	nC
Q_{GS}	Gate to Source Charge		-	2.5	-	
Q_{GD}	Gate to Drain (Miller) Charge		-	3.3	-	
Source-Drain Diode Characteristics						
V_{SD}	Diode Forward Voltage ^{*2}	$I_{SD} = 20A, V_{GS} = 0V$	-	0.9	1.2	V
t_{rr}	Reverse Recovery Time	$I_{SD} = 8A, V_{GS} = 0V$ $di/dt = 100A/\mu s$	-	110	-	ns
Q_{rr}	Reverse Recovery Charge		-	53	-	nC

Electrical Characteristics-TR2 (@ T_A = 25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
V _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = -250μA	-30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = -30V, V _{GS} = 0V	-	-	-1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} = ±20V, V _{DS} = 0V	-	-	±100	nA
On Characteristics						
R _{DS(ON)}	Drain-Source On-resistance ^{*2}	V _{GS} = -10V, I _D = -10A	-	13	20	mΩ
		V _{GS} = -4.5V, I _D = -6A	-	20	32	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = -250μA	-1	-1.7	-2.5	V
R _G	Gate Resistance	V _{GS} = 0V, f = 1MHz	-	9	-	Ω
Dynamic Characteristics						
C _{ISS}	Input Capacitance	V _{GS} = 0V	-	1406	-	pF
C _{OSS}	Output Capacitance	V _{DS} = -15V	-	157	-	
C _{RSS}	Reverse Transfer Capacitance	f = 1.0MHz	-	142	-	
Switching Characteristics						
t _{d(ON)}	Turn-on Delay Time ^{*4}	V _{DD} = -15V	-	12	-	ns
t _r	Turn-on Rise Time ^{*4}	V _{GS} = -10V	-	18	-	
t _{d(OFF)}	Turn-Off Delay Time ^{*4}	I _D = -20A	-	90	-	
t _f	Turn-Off Fall Time ^{*4}	R _G = 3Ω	-	63	-	
Q _G	Total Gate-Charge	V _{DD} = -15V	-	30.5	-	nC
Q _{GS}	Gate to Source Charge	V _{GS} = -10V	-	4.6	-	
Q _{GD}	Gate to Drain (Miller) Charge	I _D = -10A	-	5.7	-	
Source-Drain Diode Characteristics						
V _{SD}	Diode Forward Voltage ^{*2}	I _{SD} = -10A, V _{GS} = 0V	-	-0.87	-1.2	V
t _{rr}	Reverse Recovery Time	I _{SD} = -10A, V _{GS} = 0V	-	92	-	ns
Q _{rr}	Reverse Recovery Charge	di/dt = 100A/μs	-	62	-	nC

Notes:

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper
- The data tested by pulsed, pulse width ≤ 300μs, duty cycle ≤ 2%
- The E_{AS} data shows Max. rating. The test condition is N: V_{DD} = 15V, V_{GS} = 10V, L = 0.5mH
P: V_{DD} = -15V, V_{GS} = -10V, L = 0.5mH
- Guaranteed by design, not subject to production

Ratings and Characteristics Curves-TR1 (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

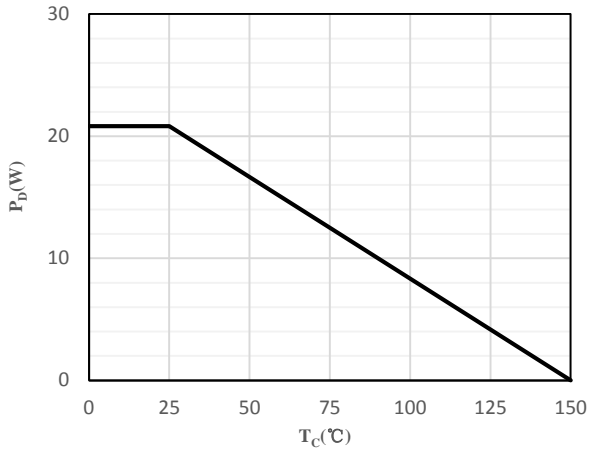


Fig 1 Power Dissipation

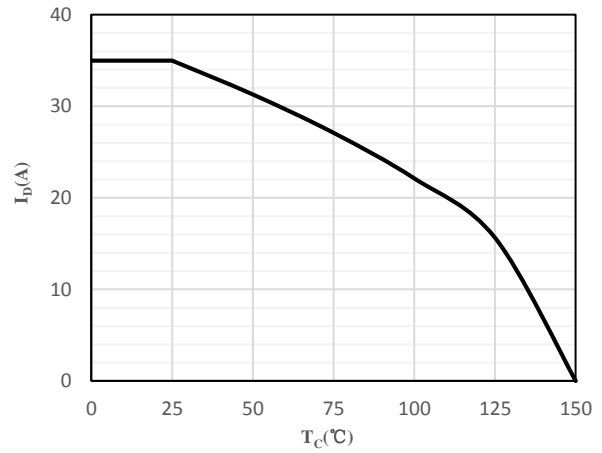


Fig 2 Drain Current

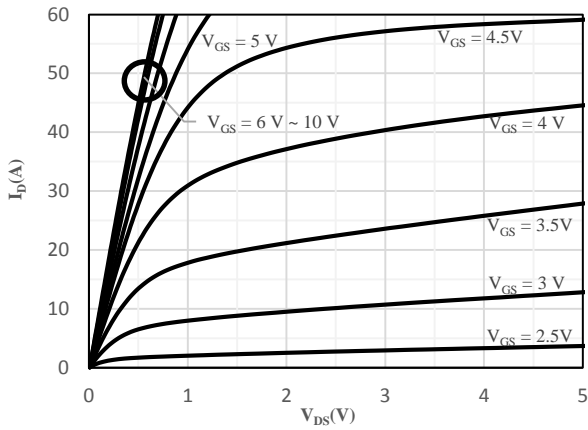


Fig 3 Typical Output Characteristics

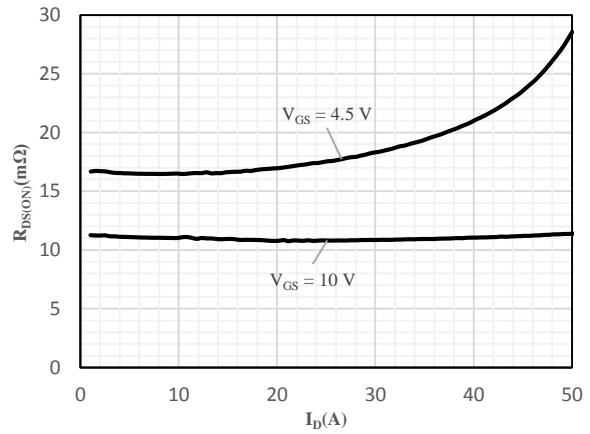


Fig 4 On-Resistance vs. Drain Current and Gate Voltage

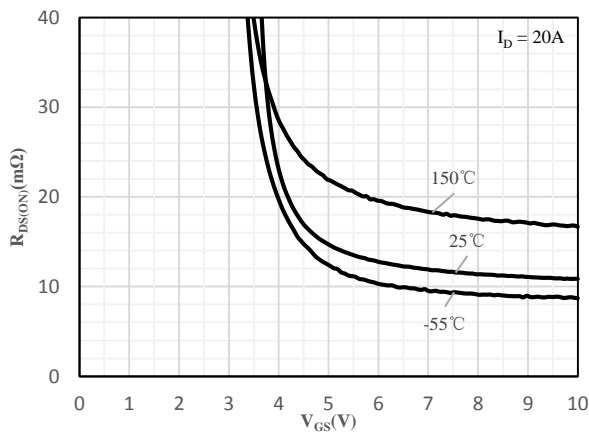


Fig 5 On-Resistance vs. Gate-Source Voltage

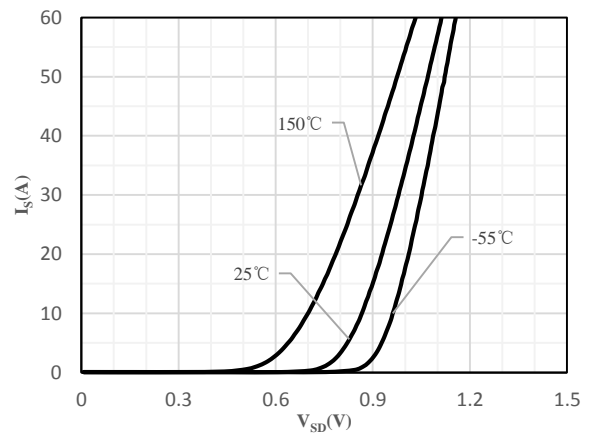


Fig 6 Body-Diode Characteristics

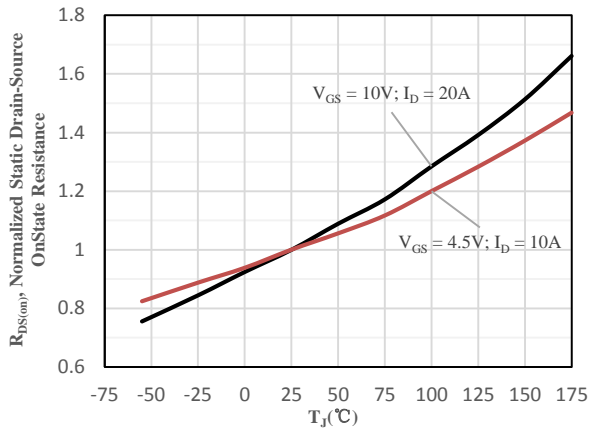


Fig 7 Normalized On-Resistance vs. Junction Temperature

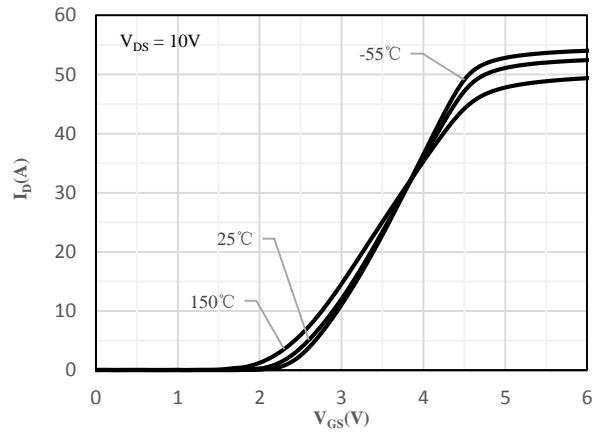


Fig 8 Transfer Characteristics

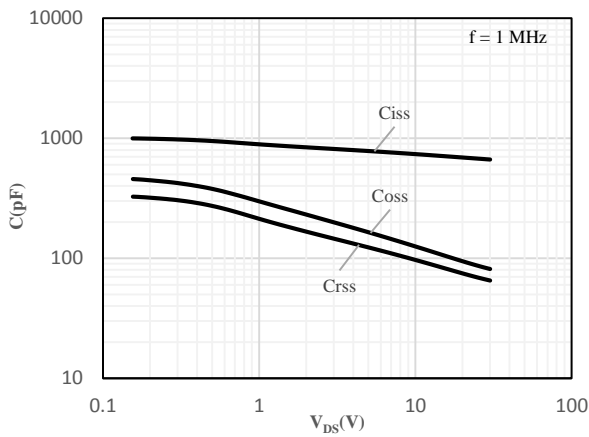


Fig 9 Capacitance Characteristics

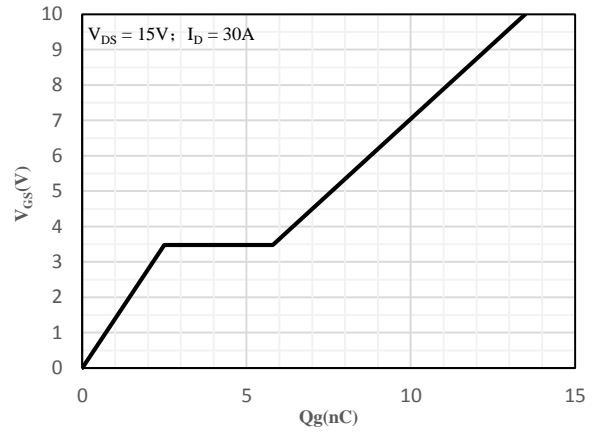


Fig 10 Gate-Charge Characteristics

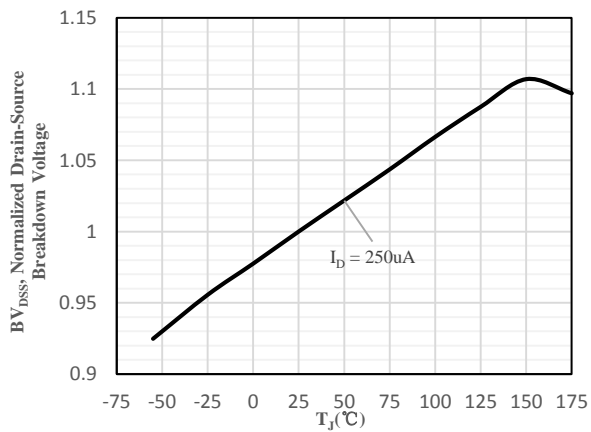


Fig 11 Normalized Breakdown Voltage vs. Junction Temperature

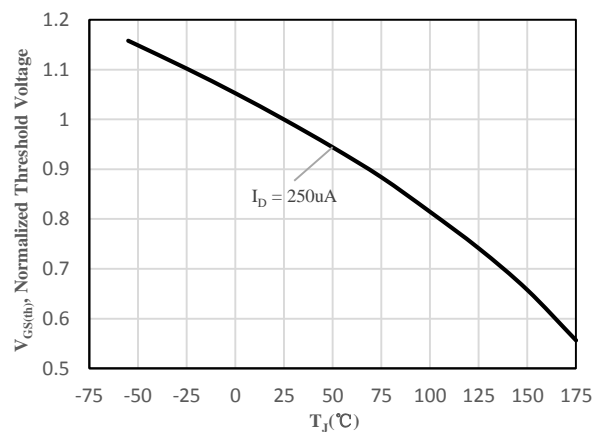


Fig 12 Normalized $V_{GS(th)}$ vs. Junction Temperature

Ratings and Characteristics Curves-TR2 (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

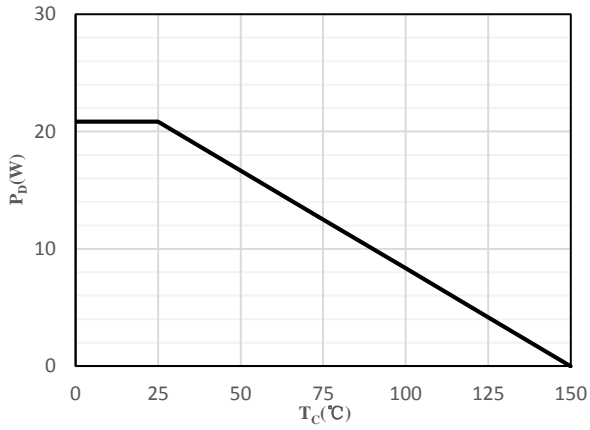


Fig 1 Power Dissipation

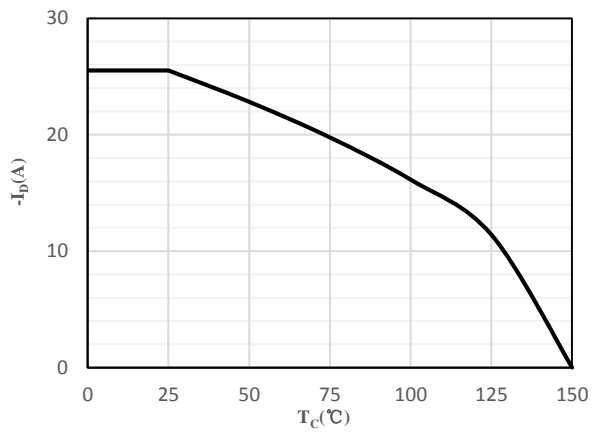


Fig 2 Drain Current

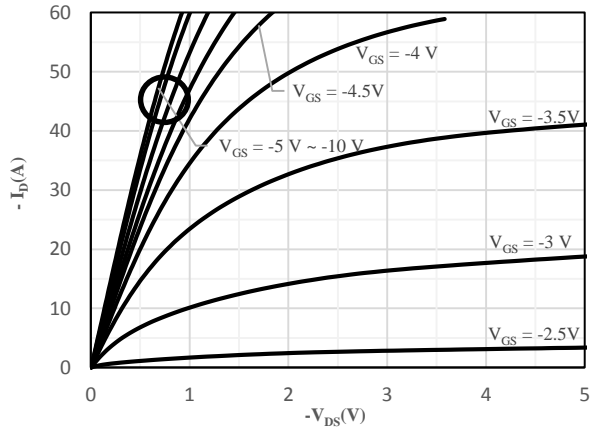


Fig 3 Typical Output Characteristics

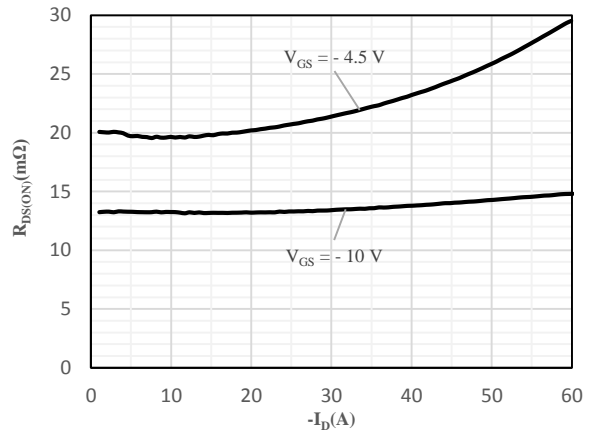


Fig 4 On-Resistance vs. Drain Current and Gate Voltage

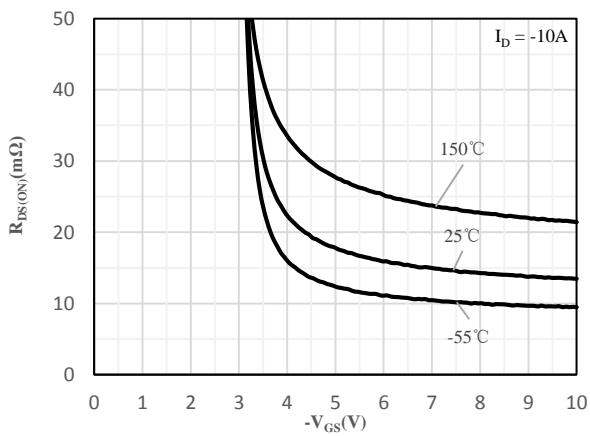


Fig 5 On-Resistance vs. Gate-Source Voltage

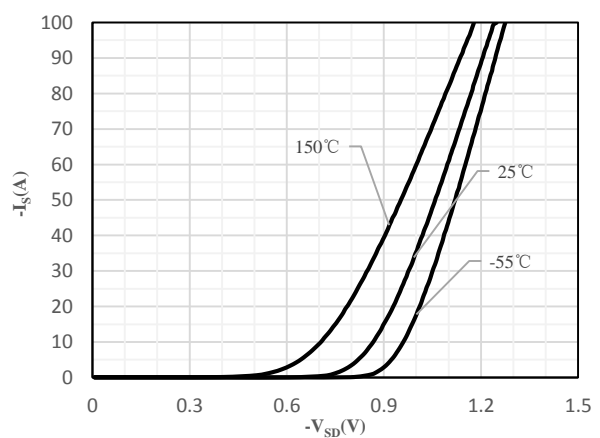


Fig 6 Body-Diode Characteristics

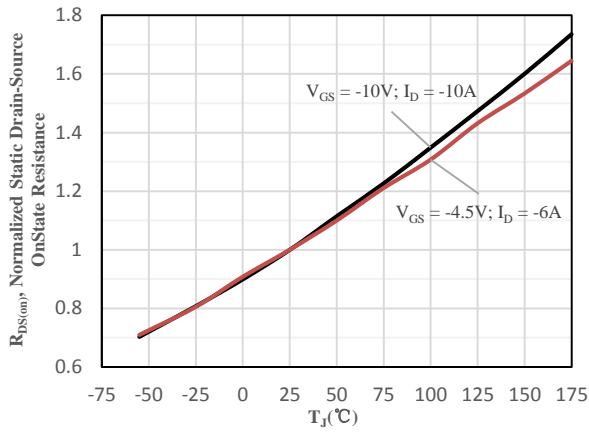


Fig 7 Normalized On-Resistance vs. Junction Temperature

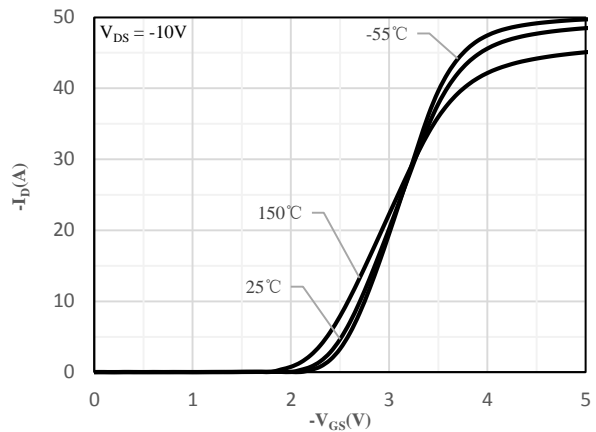


Fig 8 Transfer Characteristics

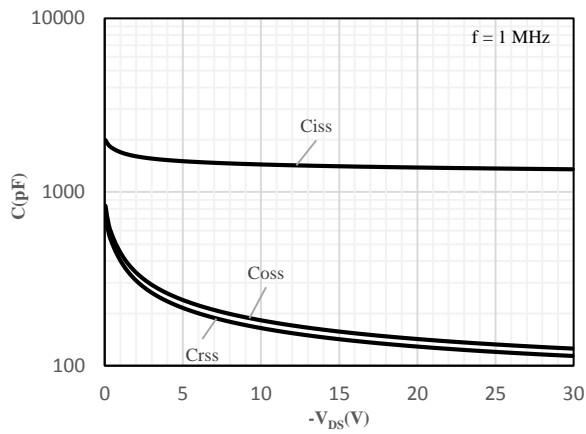


Fig 9 Capacitance Characteristics

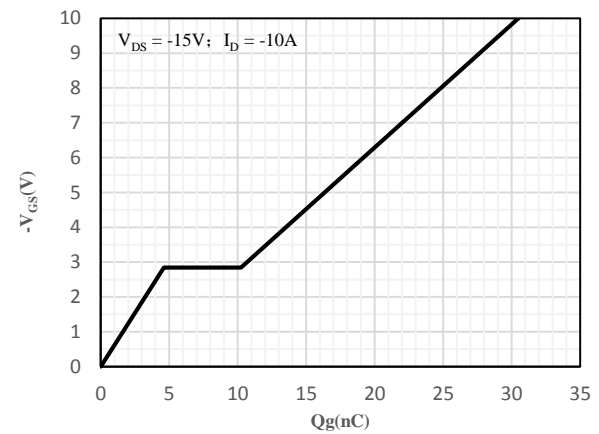


Fig 10 Gate-Charge Characteristics

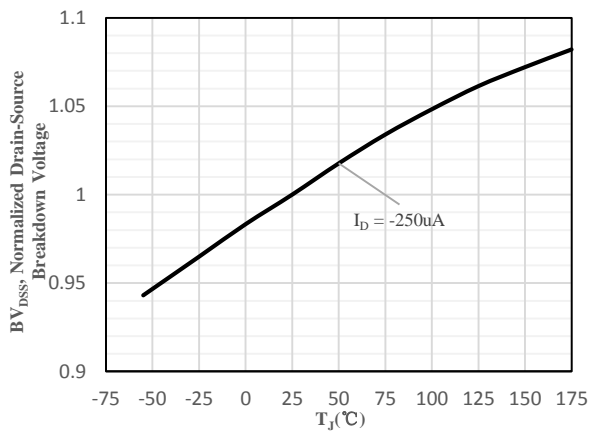


Fig 11 Normalized Breakdown Voltage vs. Junction Temperature

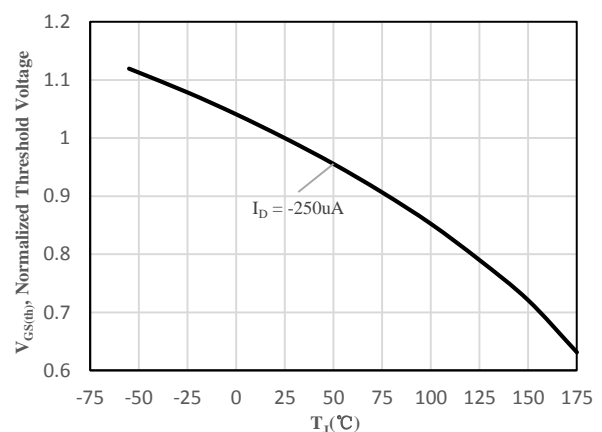
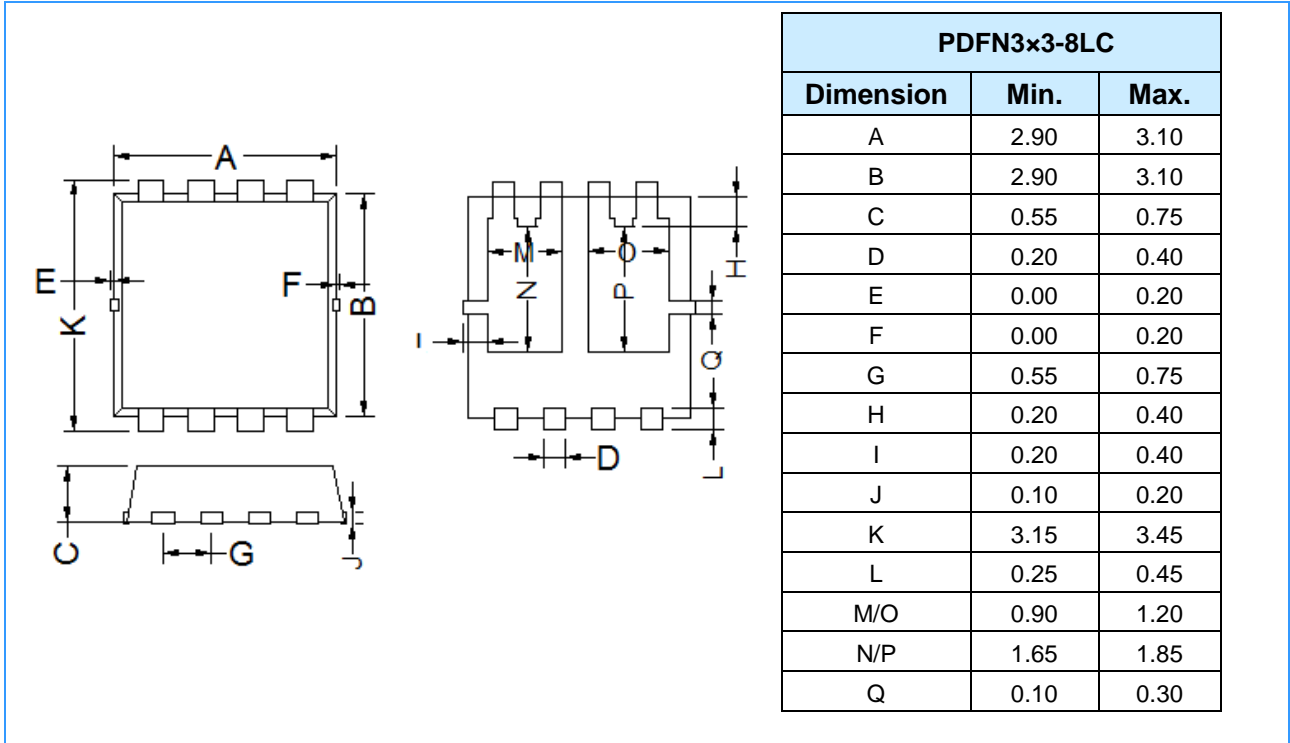


Fig 12 Normalized $V_{GS(th)}$ vs. Junction Temperature

Package Outline Dimensions (Unit: mm)



Mounting Pad Layout (Unit: mm)

