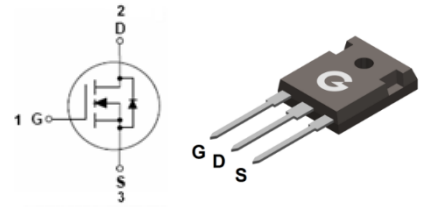


Features

- Proprietary new Trench technology
- Low gate charge minimize switching loss
- Fast recovery body diode

HF



TO-247

Mechanical Data

- Case: TO-247
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
BL760N20HU	TO-247	30 pcs / Tube	760N20HU

Maximum Ratings (@ T_c = 25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V _{DSS}	200	V
Gate-to-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current (T _c = 25°C)	I _D	30	A
Continuous Drain Current (T _c = 100°C)		21	A
Pulsed Drain Current (V _{GS} = 10V) ^{*1,2}	I _{DM}	120	A
Single Pulse Avalanche Energy (L = 0.3mH)	E _{AS}	273	mJ

Thermal Characteristics

Parameter	Symbol	Value	Unit
Power Dissipation (T _c = 25°C)	P _D	179	W
Thermal Resistance Junction-to-Air	R _{θJA}	40	°C/W
Thermal Resistance Junction-to-Case	R _{θJC}	0.7	°C/W
Operating Junction Temperature Range	T _J	-55 ~ +150	°C
Storage Temperature Range	T _{STG}	-55 ~ +150	°C

Electrical Characteristics (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
V_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	200	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 160V, V_{GS} = 0V$	-	-	1	μA
		$V_{DS} = 160V, V_{GS} = 0V, T_J = 125^\circ\text{C}$	-	-	50	
I_{GSS}	Gate-Body Leakage Current	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	± 100	nA
On Characteristics ³						
$R_{DS(ON)}$	Static Drain-Source On-resistance	$V_{GS} = 10V, I_D = 30A$	-	-	76	m Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	3	-	4.5	V
Dynamic Characteristics						
R_G	Gate Series Resistance	$f = 1.0\text{MHz}$	-	2.6	-	Ω
C_{ISS}	Input Capacitance	$V_{GS} = 0V$	-	2444	-	pF
C_{OSS}	Output Capacitance	$V_{DS} = 25V$	-	129	-	
C_{RSS}	Reverse Transfer Capacitance	$f = 1.0\text{MHz}$	-	24	-	
Switching Characteristics						
$t_{d(ON)}$	Turn-on Delay Time	$V_{DD} = 100V$ $V_{GS} = 10V$ $R_G = 2.5\Omega$ $I_D = 30A$	-	30	-	ns
t_r	Turn-on Rise Time					
$t_{d(OFF)}$	Turn-Off Delay Time					
t_f	Turn-Off Fall Time					
Q_G	Total Gate-Charge	$V_{DD} = 100V$ $I_D = 20A$ $V_{GS} = 10V$	-	53	-	nC
Q_{GS}	Gate to Source Charge					
Q_{GD}	Gate to Drain (Miller) Charge					
Source-Drain Diode Characteristics						
V_{SD}	Diode Forward Voltage	$I_{SD} = 30A, V_{GS} = 0V$	-	-	1.3	V
t_{rr}	Reverse recovery time	$V_{GS} = 0V, I_F = 30A, di/dt = 100A/\mu s$	-	50	-	ns
Q_{rr}	Reverse recovery charge		-	80	-	nC

Notes:

1. Silicon limited current only
2. Repetitive rating; pulse width limited by maximum junction temperature
3. The data tested by pulsed, pulse width $\leq 380\mu s$, duty cycle $\leq 2\%$

Ratings and Characteristics Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

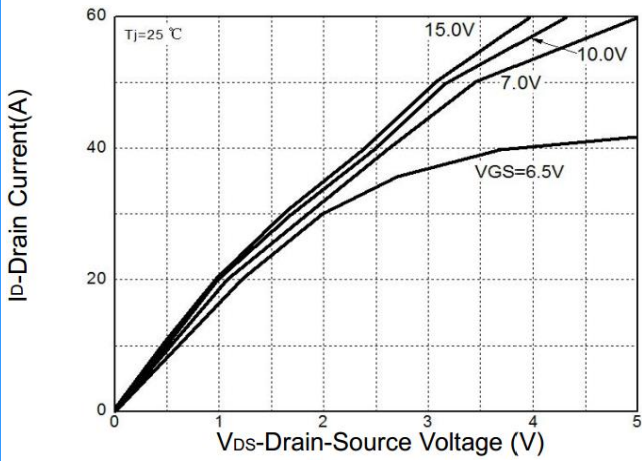


Fig 1 Typical Output Characteristics

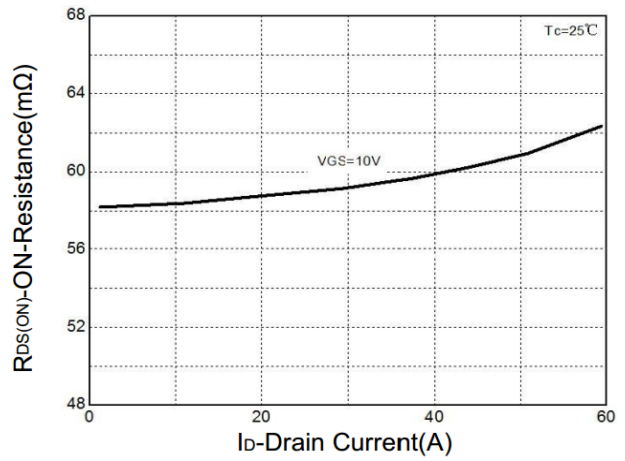


Fig 2 On-Resistance vs. Drain Current and Gate Voltage

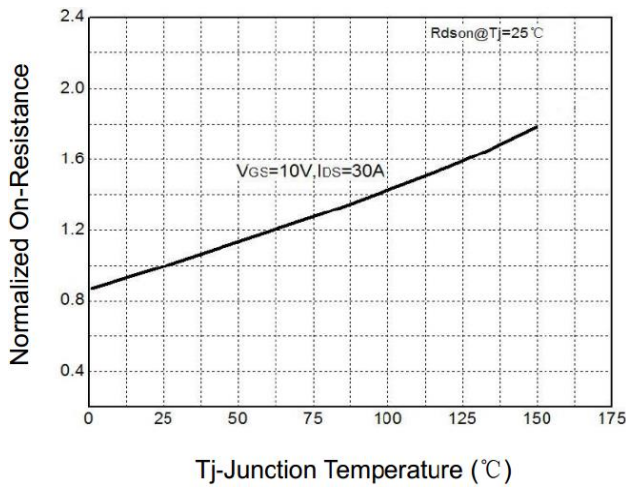


Fig 3 Normalized On-Resistance vs. Junction Temperature

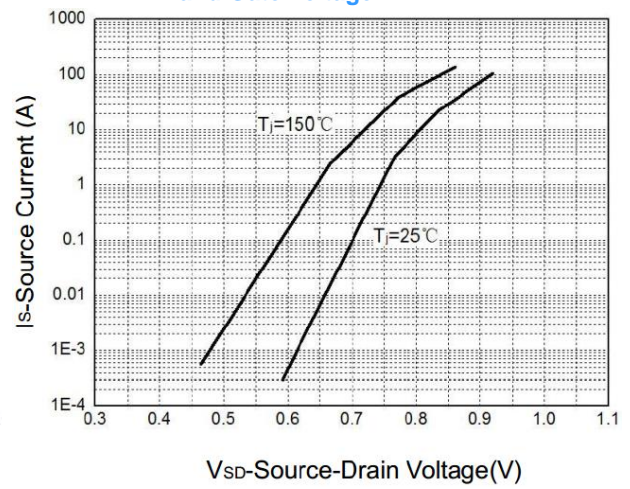


Fig 4 Body-Diode Characteristics

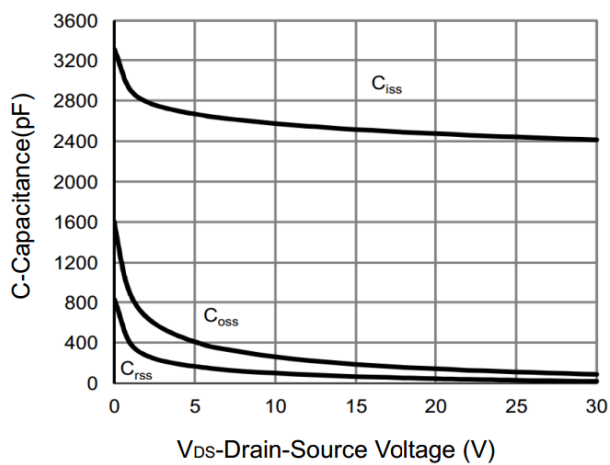


Fig 5 Capacitance Characteristics

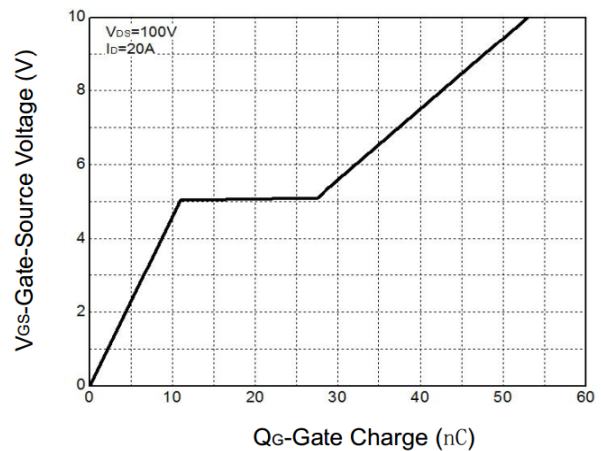


Fig 6 Gate-Charge Characteristics

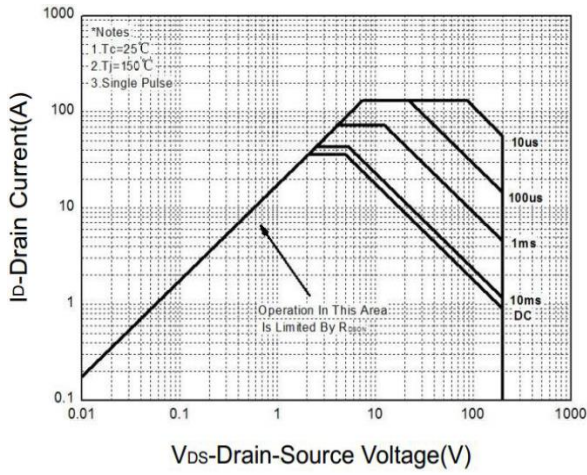


Fig 7 Safe Operation Area

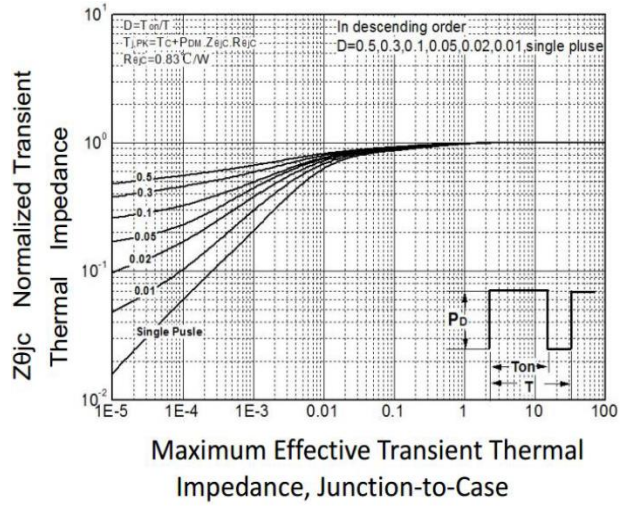
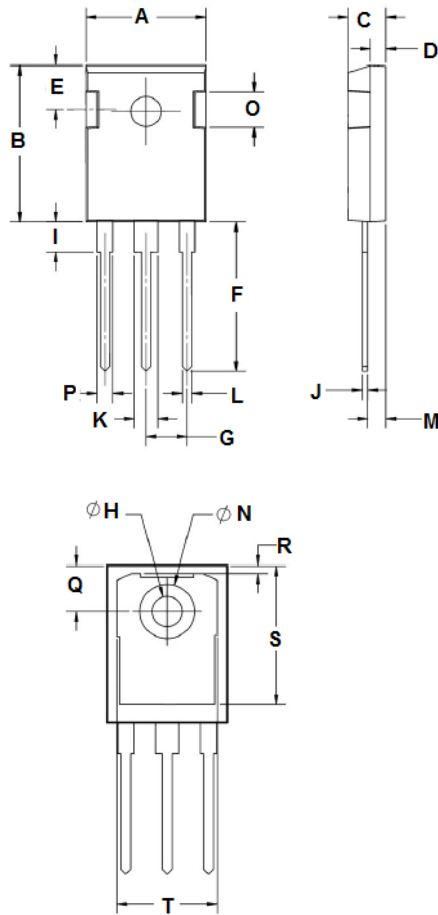


Fig 8 Thermal Transient Impedance

Package Outline Dimensions (Unit: mm)



TO-247		
Dimension	Min.	Max.
A	15.50	16.10
B	20.70	21.30
C	4.70	5.30
D	1.80	2.20
E	5.20	5.80
F	19.70	20.30
G	5.20	5.60
H	3.30	3.70
I	3.90	4.30
J	0.50	0.70
K	2.80	3.20
L	1.00	1.40
M	2.20	2.60
N	7.00	7.20
O	4.90	5.30
P	1.80	2.20
Q	5.70	5.90
R	0.80	1.20
S	17.00	17.80
T	13.60	14.20

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