

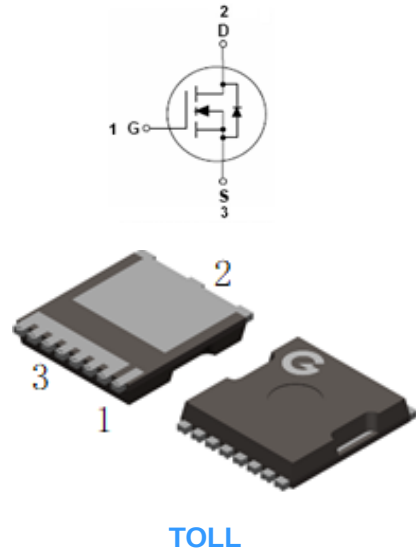
Features

- Typical $R_{DS(ON)} = 2.5m\Omega$ at $V_{GS} = 10V$, $I_D = 80 A$
- Typical $Q_g(tot) = 89 nC$ at $V_{GS} = 10 V$, $I_D = 80 A$
- UIS capability

HF

Mechanical Data

- Case: TOLL
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208



Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
BL030N085TH-TL	TOLL	2000 pcs / Tape & Reel	030N085TH

Maximum Ratings (@ $T_J = 25^\circ C$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DSS}	85	V
Gate-to-Source Voltage	V_{GSS}	± 20	V
Continuous Drain Current ($T_C = 25^\circ C$, $V_{GS} = 10V$) *1	I_D	220	A
Single Pulse Avalanche Energy *2	E_{AS}	205	mJ

Thermal Characteristics

Parameter	Symbol	Value	Unit
Power Dissipation ($T_C = 25^\circ C$)	P_D	300	W
Thermal Resistance Junction-to-Air *3	$R_{\theta JA}$	43	$^\circ C/W$
Thermal Resistance Junction-to-Case	$R_{\theta JC}$	0.5	$^\circ C/W$
Operating Junction Temperature Range	T_J	$-55 \sim +175$	$^\circ C$
Storage Temperature Range	T_{STG}	$-55 \sim +175$	$^\circ C$

Electrical Characteristics (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
V _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	85	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 80V, V _{GS} = 0V, T _J = 25°C	-	-	1	μA
		V _{DS} = 80V, V _{GS} = 0V, T _J = 175°C *4	-	-	1	mA
I _{GSS}	Gate-Body Leakage Current	V _{GS} = ±20V, V _{DS} = 0V	-	-	±100	nA
On Characteristics						
R _{DS(ON)}	Static Drain-Source On-resistance	V _{GS} = 10V, I _D = 80A, T _J = 25°C	-	-	3	mΩ
		V _{GS} = 10V, I _D = 80A, T _J = 175°C *4	-	4.9	-	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	2	2.8	4	V
R _G	Gate Resistance	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz	-	3	-	Ω
Dynamic Characteristics						
C _{ISS}	Input Capacitance	V _{GS} = 0V	-	6313	-	pF
C _{OSS}	Output Capacitance	V _{DS} = 40V	-	1249	-	
C _{RSS}	Reverse Transfer Capacitance	f = 250kHz	-	28	-	
Q _G	Total Gate-Charge	V _{DD} = 64V	-	89	-	nC
Q _{GS}	Gate to Source Charge	V _{GS} = 10V	-	30	-	
Q _{GD}	Gate to Drain (Miller) Charge	I _D = 80A	-	20	-	
Switching Characteristics						
t _{d(ON)}	Turn-on Delay Time	V _{DD} = 40V	-	37	-	ns
t _r	Turn-on Rise Time	V _{GS} = 10V	-	30	-	
t _{d(OFF)}	Turn-Off Delay Time	R _G = 6Ω	-	87	-	
t _f	Turn-Off Fall Time	I _D = 80A	-	35	-	
Source-Drain Diode Characteristics						
V _{SD}	Diode Forward Voltage	I _{SD} = 80A, V _{GS} = 0V	-	-	1.25	V
		I _{SD} = 40A, V _{GS} = 0V	-	-	1.20	V
t _{rr}	Reverse Recovery Time	I _F = 40A, V _{DD} = 50V	-	80	-	ns
Q _{rr}	Reverse Recovery Charge	dI _F /d _t = 100A/μs	-	160	-	nC

Notes:

- Current is limited by silicon
- Starting $T_J = 25^\circ\text{C}$, $L = 0.1\text{mH}$, $V_{DD} = 64V$
- The maximum rating presented here is based on mounting on a 1 in2 pad of 2oz copper
- Product is not tested to this condition in production

Ratings and Characteristics Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

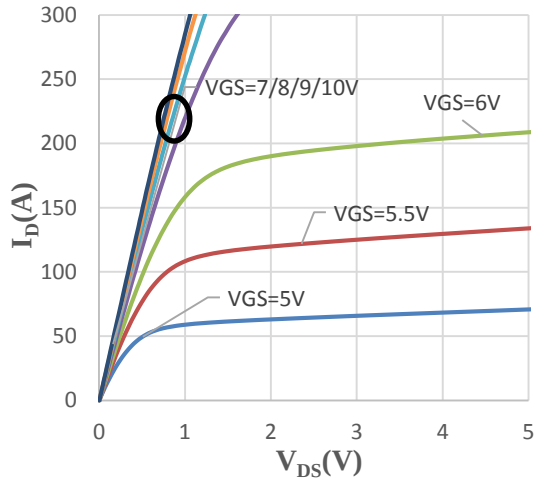


Fig 1 Typical Output Characteristics

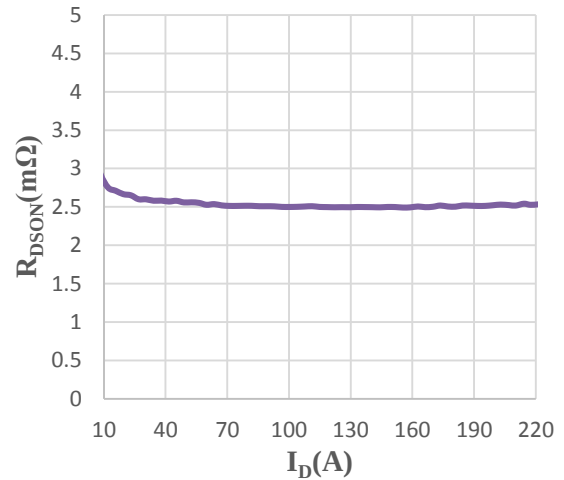


Fig 2 On-Resistance vs. Drain Current

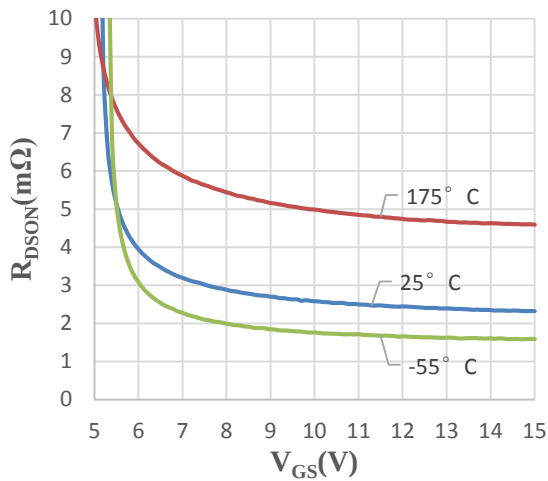


Fig 3 On-Resistance vs. Gate-Source Voltage

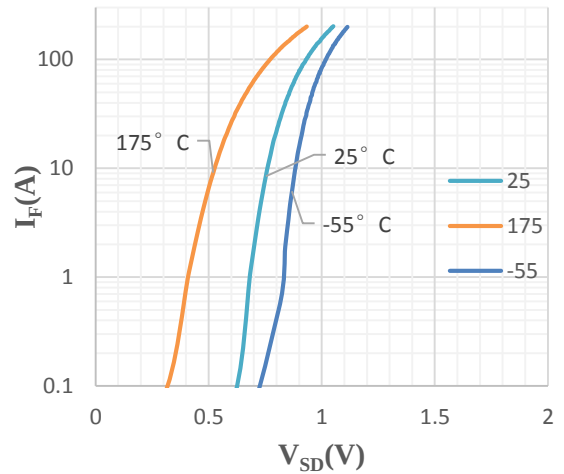


Fig 4 Body-Diode Characteristics

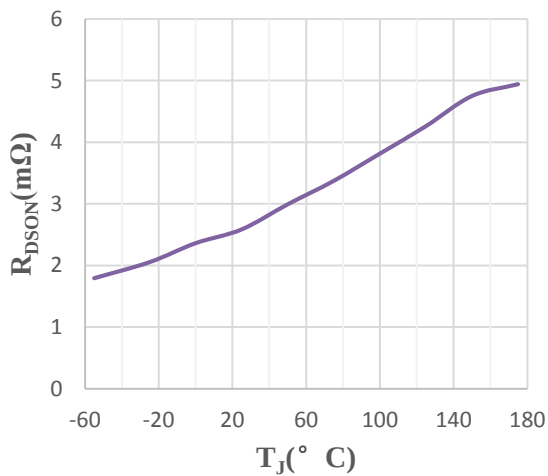


Fig 5 On-Resistance vs. Junction Temperature

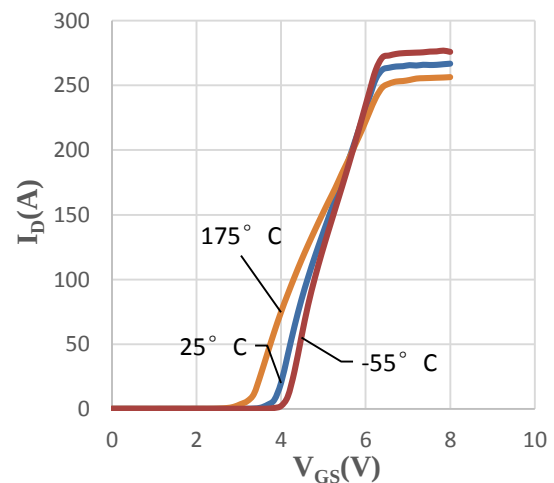


Fig 6 Transfer Characteristics

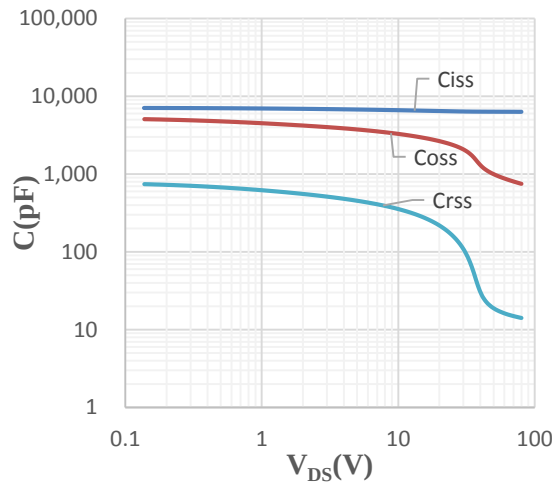


Fig 7 Capacitance Characteristics

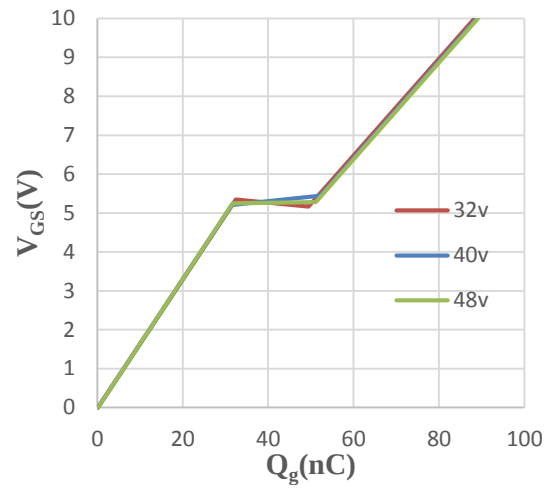


Fig 8 Gate-Charge Characteristics

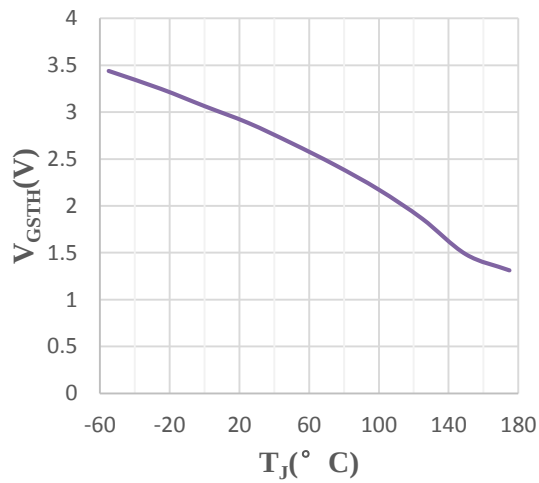


Fig 9 Gate Threshold Voltage vs Temperature

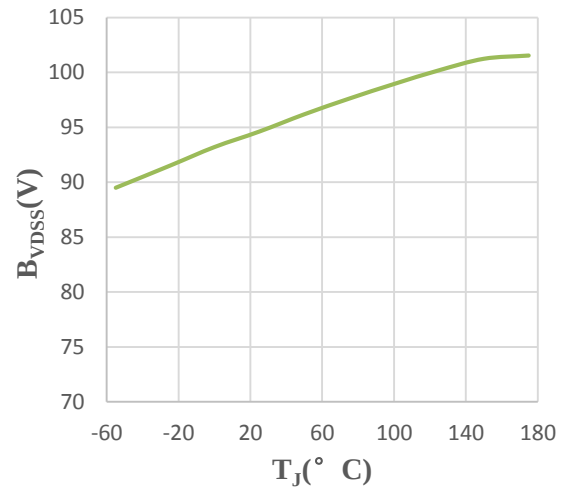
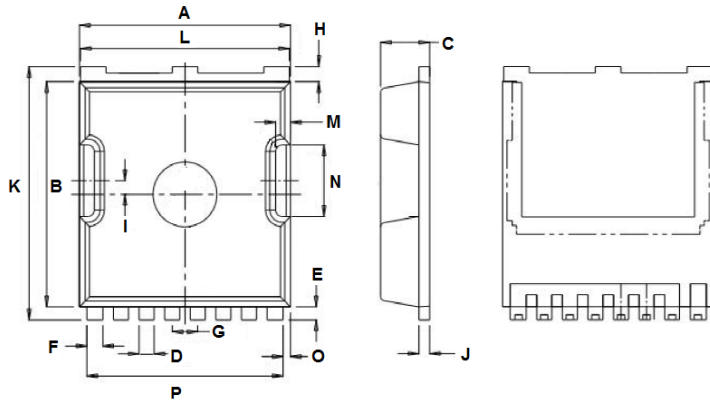


Fig 10 BV_{DS} vs Junction temperature

Package Outline Dimensions (Unit: mm)



TOLL		
Dimension	Min.	Max.
A	9.850	9.950
B	10.350	10.450
C	2.250	2.350
D	0.600	0.800
E	0.500	0.700
F	0.650	0.850
G	1.100	1.300
H	0.600	0.800
I	0.580	0.680
J	0.492	0.508
K	11.600	11.800
L	9.750	9.850
M	0.550	0.650
N	3.250	3.350
O	0.250	0.450
P	9.175	9.225

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