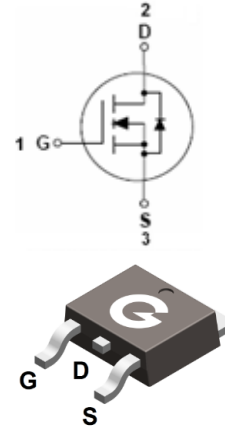


Features

- Advanced trench technology
- Low on-resistance
- Fast switching speed
- Low gate drive
- RoHS compliant with Halogen-free

HF



TO-252

Mechanical Data

- Case: TO-252
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matted-Tin plated; Solderable Per MIL-STD-202, Method 208

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
BL04N06D	TO-252	80 pcs / Tube or 2500 pcs / Tape & Reel	04N06

Maximum Ratings (@ T_A = 25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V _{DSS}	60	V
Gate-to-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current (T _C = 25°C)	I _D	8.6	A
Continuous Drain Current (T _A = 25°C) *1		4	
Continuous Drain Current (T _A = 70°C) *1		3.2	
Pulsed Drain Current (t _p = 10μs, T _A = 25°C)	I _{DM}	20	A
Single Pulse Avalanche Energy *3	E _{AS}	9	mJ
Power Dissipation (T _C = 25°C)	P _D	15	W
Power Dissipation (T _A = 25°C) *1		2	
Operating Junction Temperature Range	T _J	-55 ~ +150	°C
Storage Temperature Range	T _{STG}	-55 ~ +150	°C

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance Junction-to-Case	R _{θJC}	-	-	8.33	°C/W
Thermal Resistance Junction-to-Air *1	R _{θJA}	-	-	62	°C/W

Electrical Characteristics (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
V_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	60	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 60V, V_{GS} = 0V$	-	-	1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	± 100	nA
On Characteristics						
$R_{DS(ON)}$	Drain-Source On-resistance ^{*2}	$V_{GS} = 10V, I_D = 4A$	-	55	105	m Ω
		$V_{GS} = 4.5V, I_D = 4A$	-	72	125	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	0.8	1.6	2	V
R_G	Gate Resistance	$V_{GS} = 0V, f = 1MHz$	-	7.7	-	Ω
Dynamic Characteristics						
C_{ISS}	Input Capacitance	$V_{GS} = 0V$ $V_{DS} = 40V$ $f = 1MHz$	-	420	-	pF
C_{OSS}	Output Capacitance					
C_{RSS}	Reverse Transfer Capacitance					
Switching Characteristics						
$t_{d(ON)}$	Turn-on Delay Time ^{*4}	$V_{DD} = 30V$ $V_{GS} = 10V$ $R_G = 1\Omega$ $I_D = 1.5A$	-	6.5	-	ns
t_r	Turn-on Rise Time ^{*4}					
$t_{d(OFF)}$	Turn-Off Delay Time ^{*4}					
t_f	Turn-Off Fall Time ^{*4}					
Q_G	Total Gate-Charge	$V_{DD} = 30V$ $V_{GS} = 10V$ $I_D = 4A$	-	8.8	-	nC
Q_{GS}	Gate to Source Charge					
Q_{GD}	Gate to Drain (Miller) Charge					
Source-Drain Diode Characteristics						
V_{SD}	Diode Forward Voltage ^{*2}	$I_{SD} = 4A, V_{GS} = 0V$	-	0.85	2	V
t_{rr}	Reverse Recovery Time	$V_{GS} = 0V, I_F = 4A$ $dI_F/dt = 100A/\mu s$	-	14	-	ns
Q_{rr}	Reverse Recovery Charge		-	9	-	nC

Notes:

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper
- The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- The E_{AS} data shows Max. rating. The test condition is $V_{DD} = 30V, V_{GS} = 10V, L = 0.5mH$
- Guaranteed by design, not subject to production

Ratings and Characteristics Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

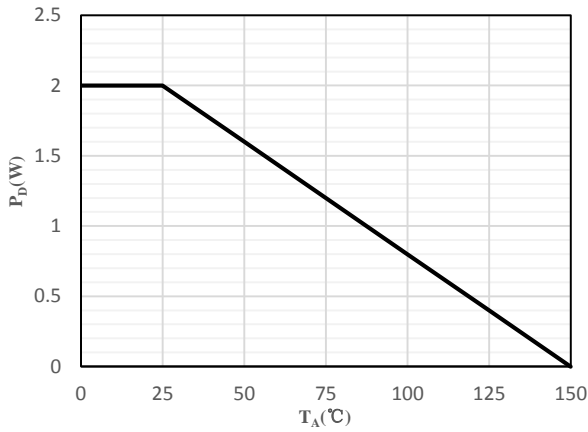


Fig 1 Power Dissipation

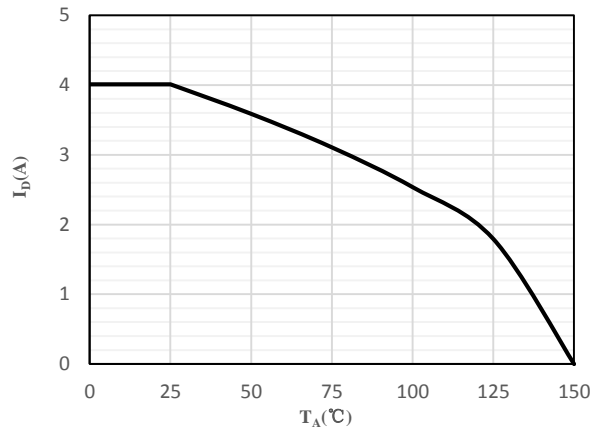


Fig 2 Drain Current

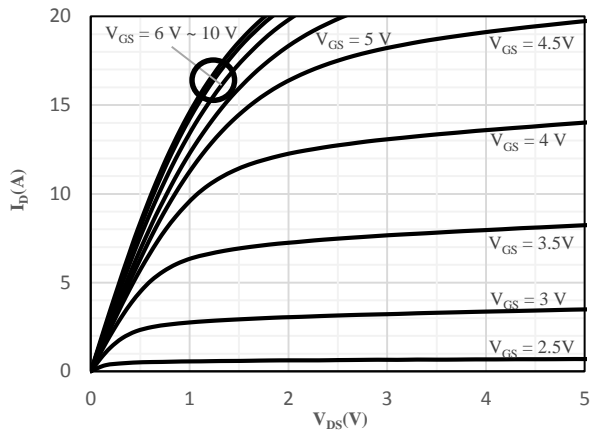


Fig 3 Typical Output Characteristics

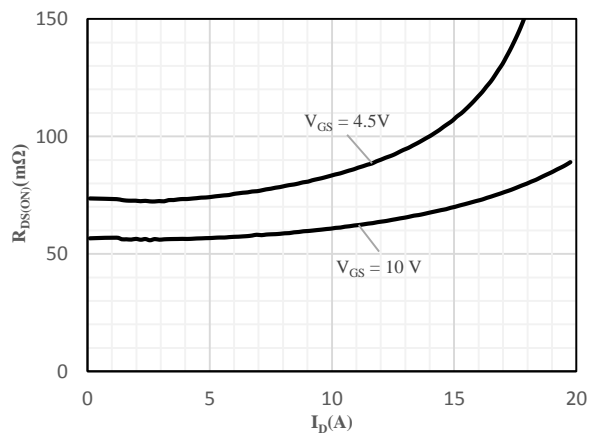


Fig 4 On-Resistance vs. Drain Current and Gate Voltage

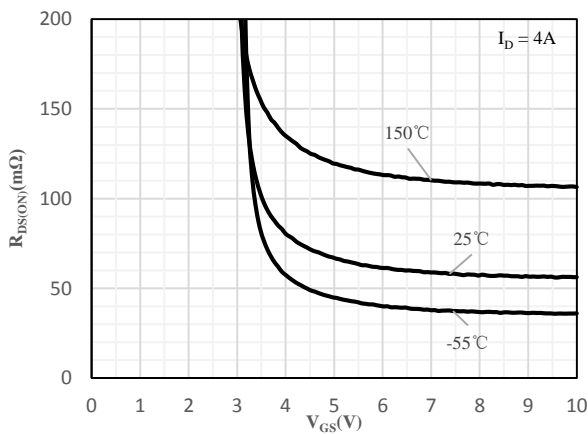


Fig 5 On-Resistance vs. Gate-Source Voltage

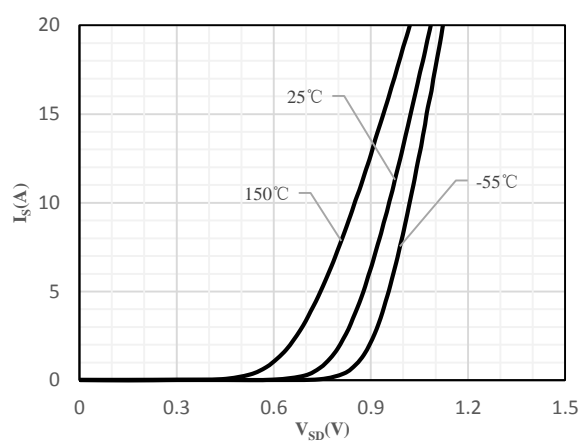


Fig 6 Body-Diode Characteristics

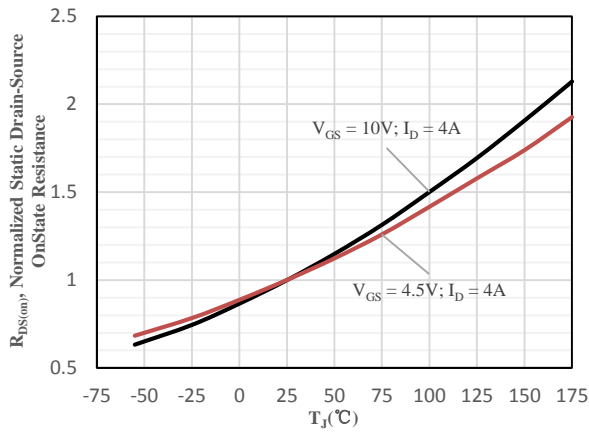


Fig 7 Normalized On-Resistance vs. Junction Temperature

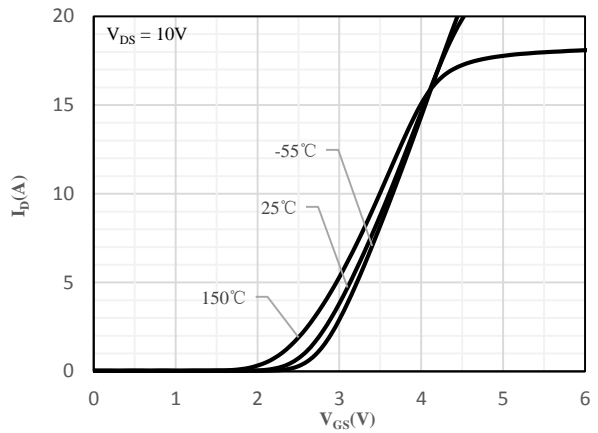


Fig 8 Transfer Characteristics

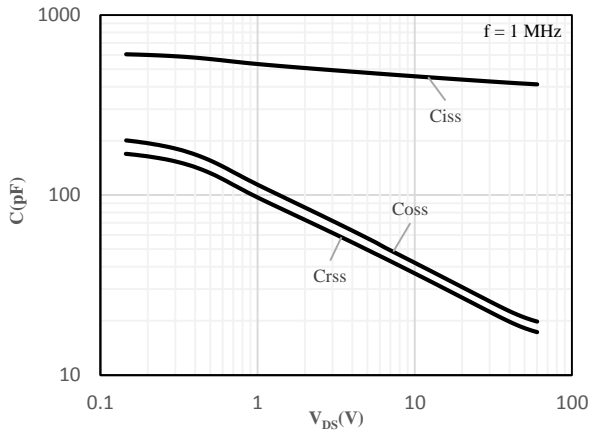


Fig 9 Capacitance Characteristics

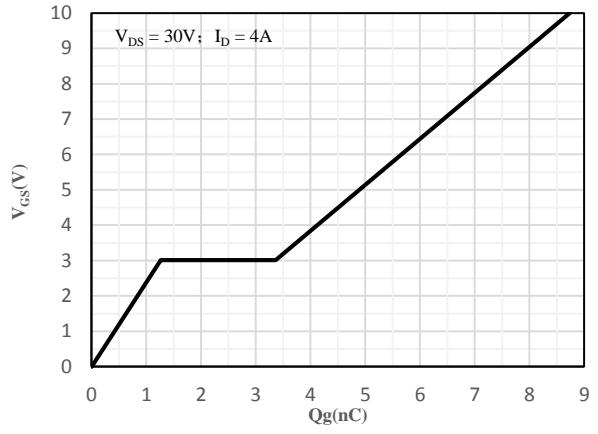


Fig 10 Gate-Charge Characteristics

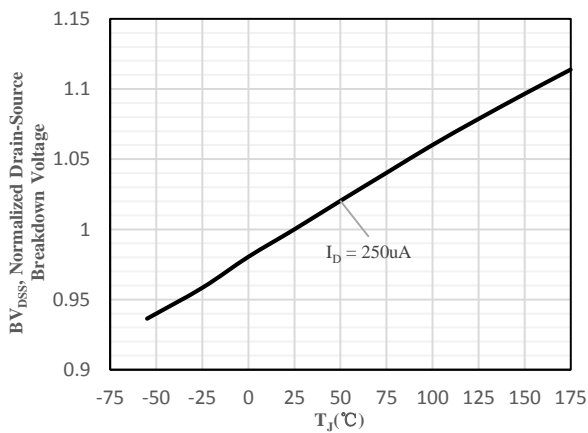


Fig 11 Normalized Breakdown Voltage vs. Junction Temperature

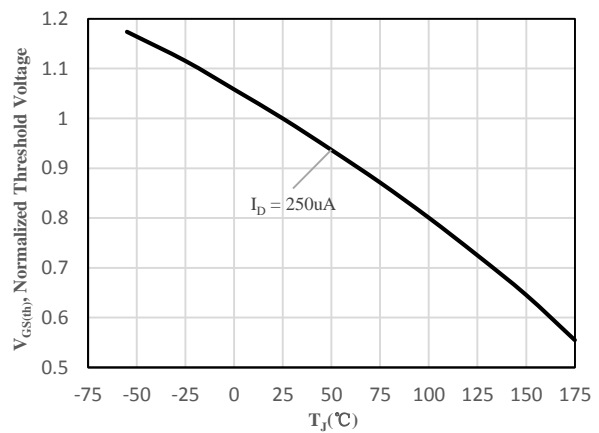
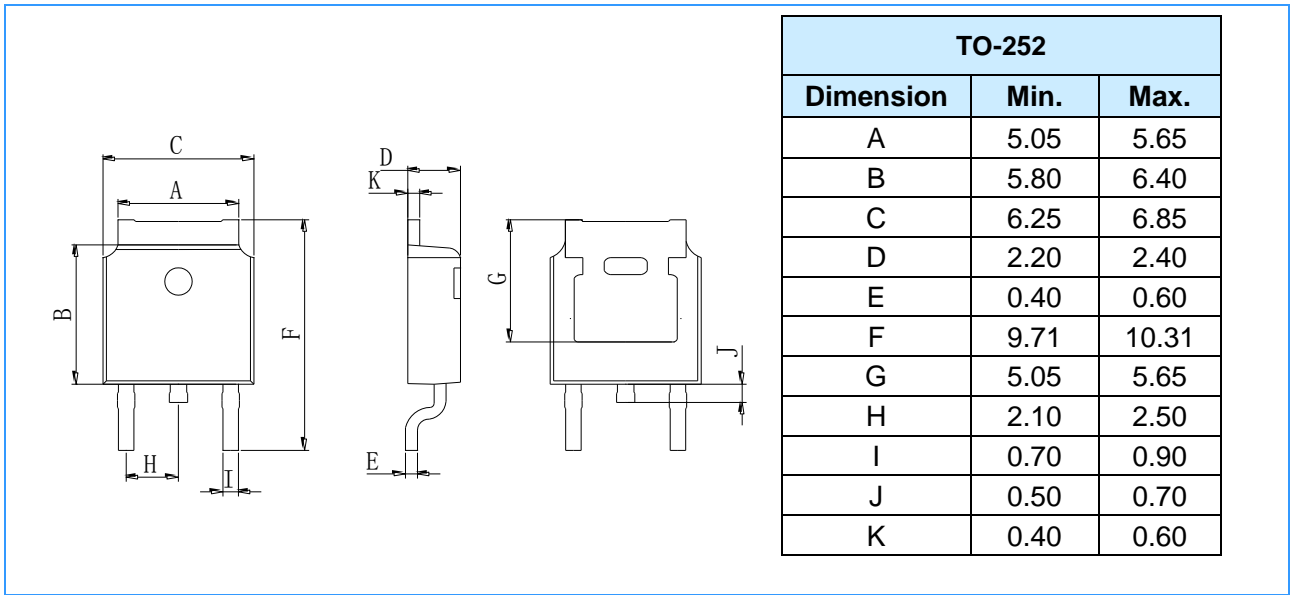


Fig 12 Normalized $V_{GS(th)}$ vs. Junction Temperature

Package Outline Dimensions (Unit: mm)



Mounting Pad Layout (Unit: mm)

